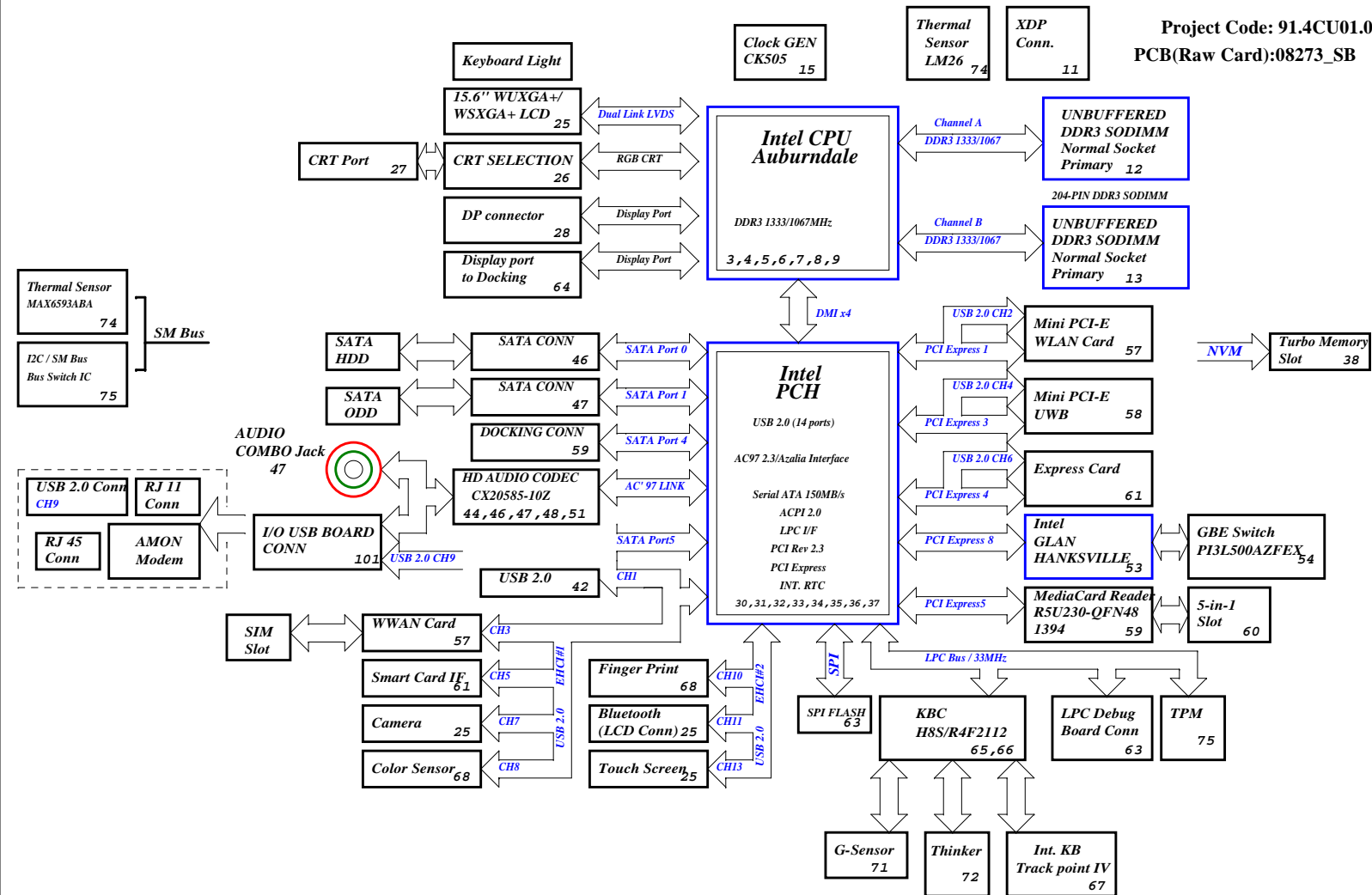


March. '9

Kendo-1 INT. Graphics Block Diagram

Project Code: 91.4CU01.001
PCB(Raw Card):08273_SB



PCB Layer Stackup

L1:TOP
L2:GND
L3:Signal1
L4:Signal2
L5:VCC
L6:Signal 3
L7:GND
L8:BOTTOM

Battery Charger/Selector
BQ24741RHDR 78

<i>INPUTS</i>	<i>OUTPUTS</i>
<i>DOCK_PWR20_F</i>	<i>M-BAT-PWR</i> <i>S-BAT-PWR</i>

System DC/DC
TPS51222RTV 82

<i>VINT20</i>	<i>VCC5M</i> <i>VCC3M</i>
---------------	------------------------------

CPU DC/DC
ADP3212MNR2G 83

<i>VINT20</i>	<i>VCCCPUCORE</i>
---------------	-------------------

VCC1R05LAN/VCC1R5A

VT356	89
VCC5V_OUT	VCC1R05AMT VCC1R5A

VCC0R75B
MAX1510ETB 89

<i>VCC1R5A</i>	<i>DDR3_VREF</i> <i>VCC0R75B</i>

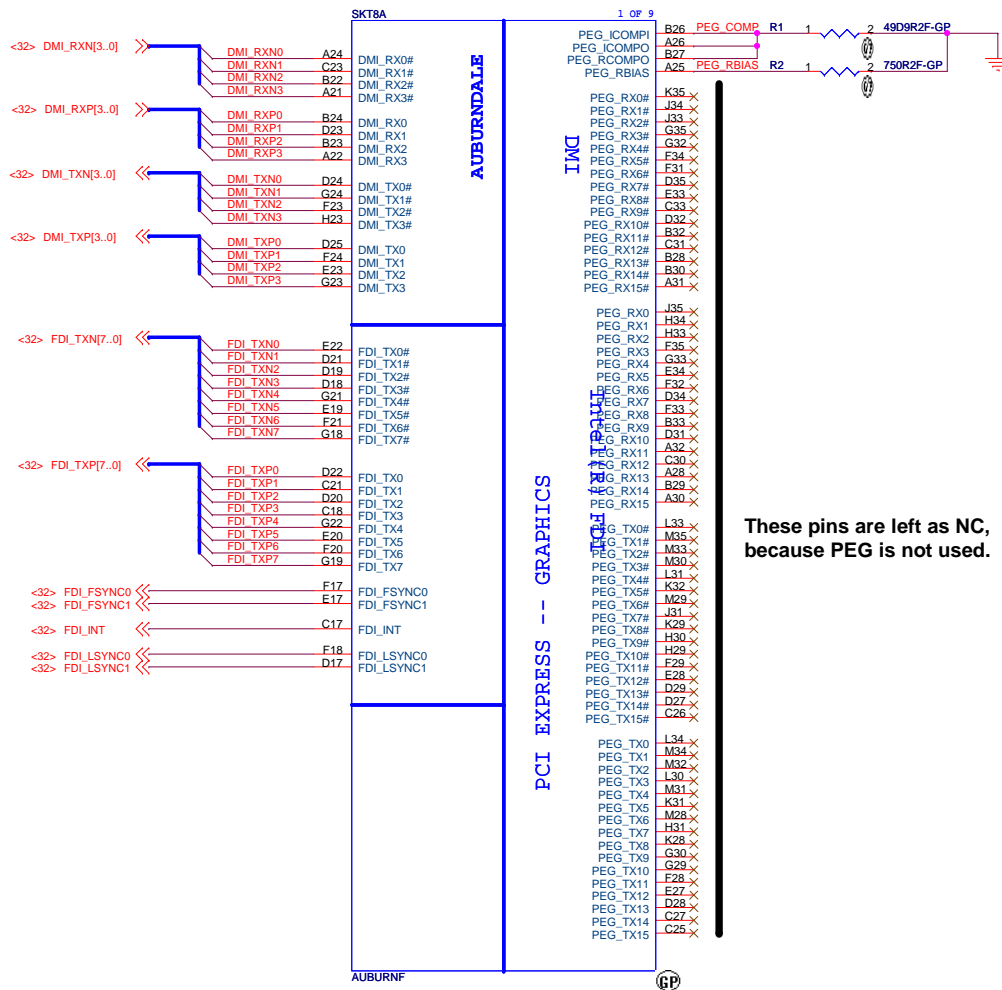
VCC1R8B

BD3551		92
VCC3M	VCC1R8B	

BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
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Title			
Block Diagram			
Size Custom	Document Number Kendo-1-UMA		Rev SB
Date:	Friday, March 13, 2009	Sheet 1 of	104



BOM1

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title CPU(1/7): DMI/PEG/FDI
Size A3 Document Number Kendo-1-UMA Rev SB
Date: Friday, March 13, 2009 Sheet 3 of 104

**No use Catastrophic Error
Option with this pin.
Thermal Protection of CPU
at High Temp is controlled by LM26.**

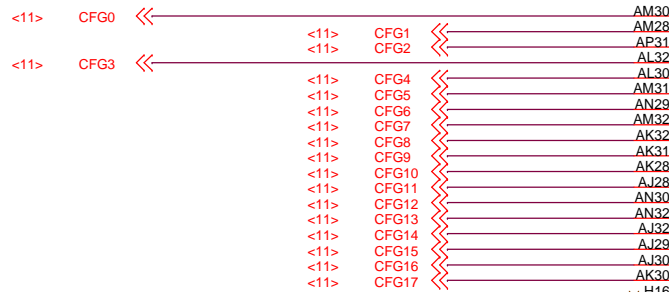


緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
CPU(2/7): CLK/MISC/JTAG			
Size	Document Number		Rev
A3		Kendo-1-UMA	SB
Date:	Friday, March 13, 2009	Sheet 4 of	104

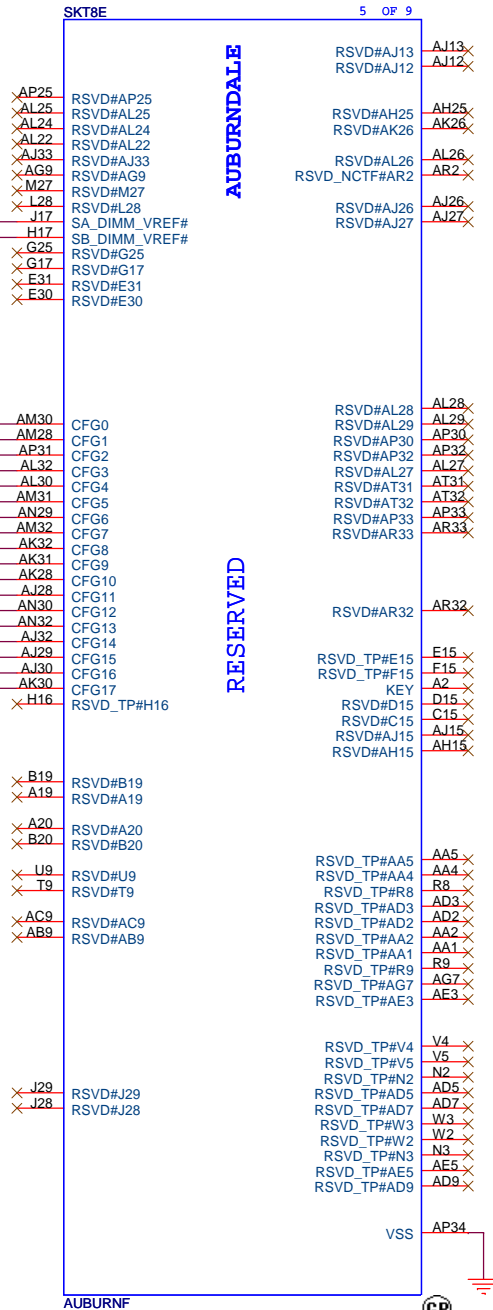
<12> SA_VREF
<13> SB_VREF

Need to confirm about Internall Pullup



CFG[1:0] => 11 : 1x16 PEG
10 : 2 x 8 PEG

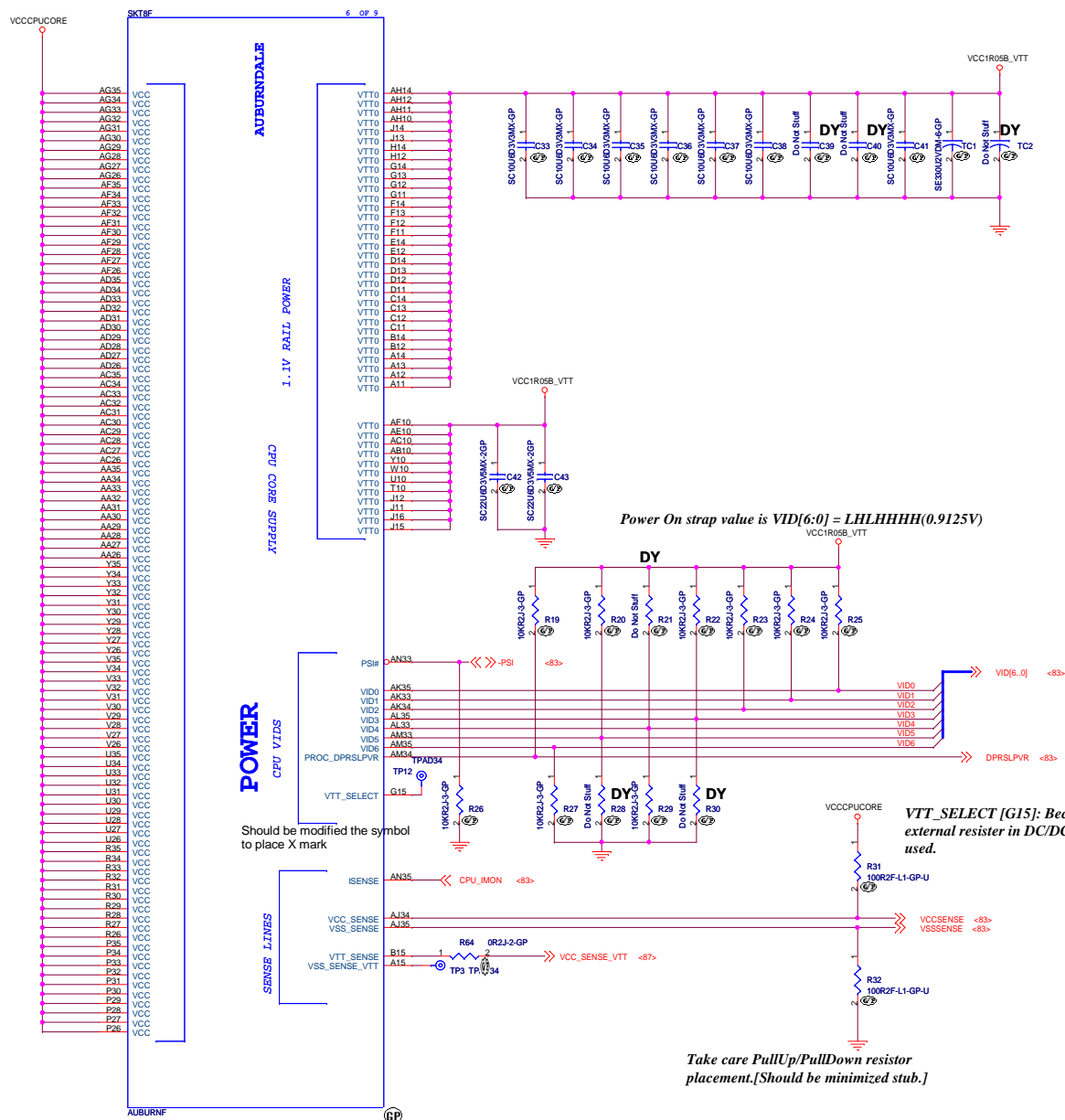
CFG3 => 1 : Normal Operation
0 : Lane Numbers Reversed.



BOM1

緯創資通		Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
CPU(4/7): CFG/RSVD			
Size	Document Number	Rev	SB
Custom			
Date:	Friday, March 13, 2009	Sheet	6 of 104

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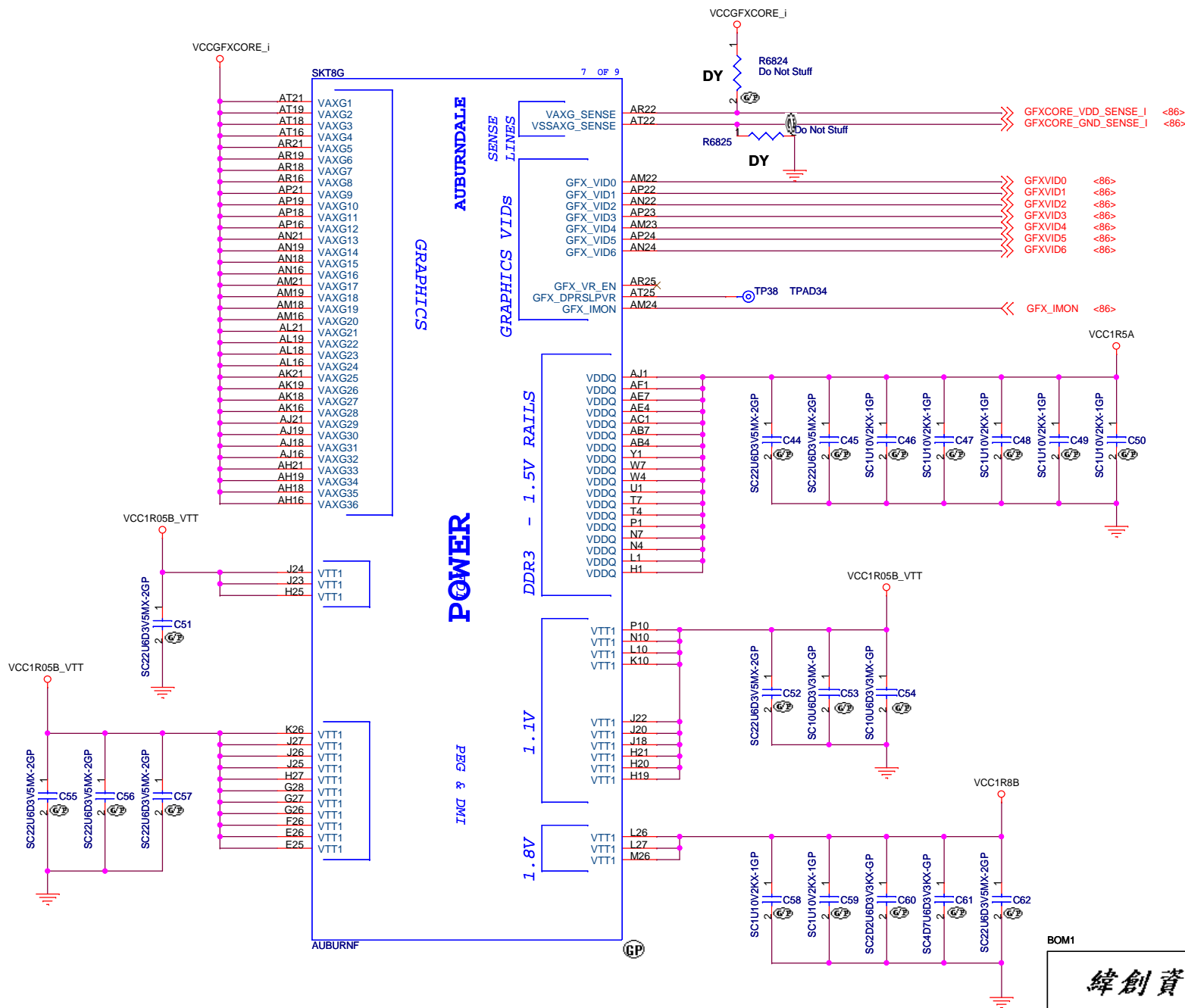


<https://Dr-Bios.com>

BOM1

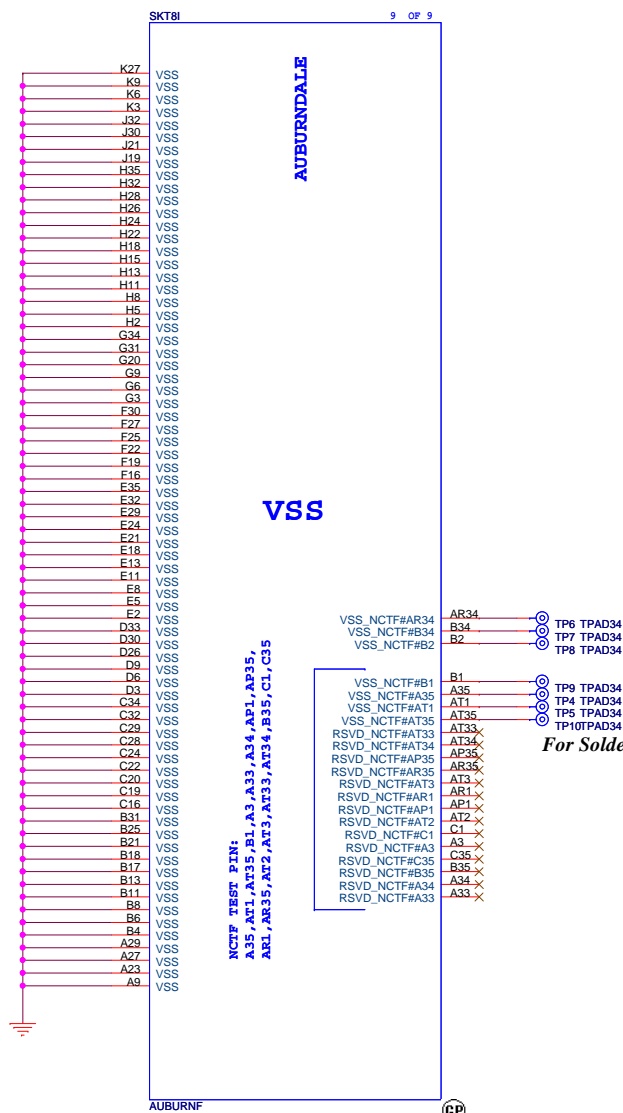
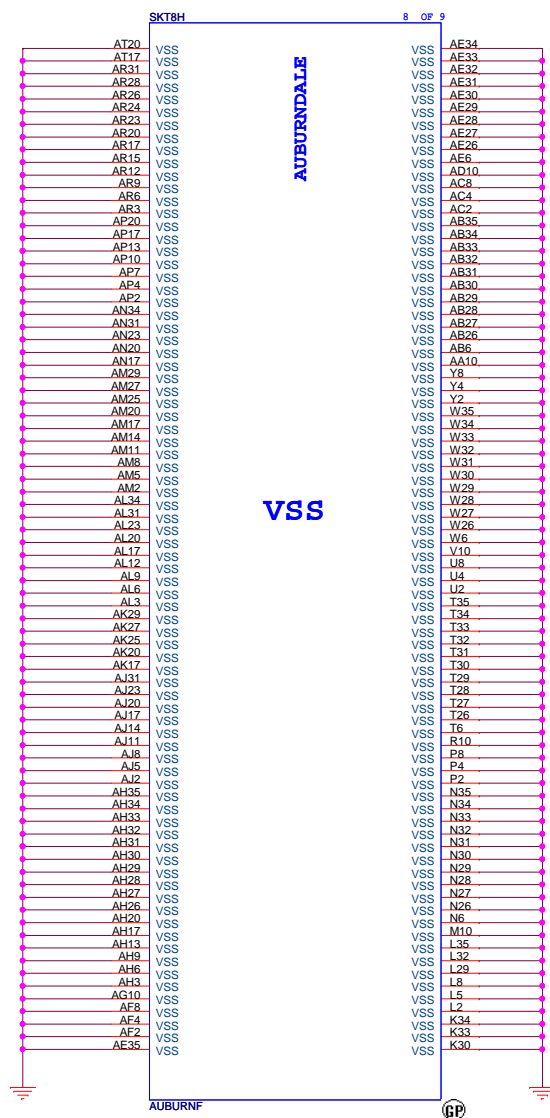
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichia, Taipei Hsien 221, Taiwan, R.O.C.

Title		CPU(5/7): VCC1	
Size	Document Number	Kendo-1-UMA	Rev
C			SB
Date:	Friday, March 13, 2009	Sheet	7 of 104



BOM1

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipai Hsien 221, Taiwan, R.O.C.	
Title			
CPU(6/7): VCC2			
Size	Document Number		Rev
Custom	Kendo-1-UMA		SB
Date:	Friday, March 13, 2009	Sheet 8 of	104



BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
CPU(7/7): GND			
Size	Document Number	Rev	
A3		Kendo-1-UMA	
Date: Friday, March 13, 2009		Sheet 9 of	104

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<https://Dr-Bios.com>

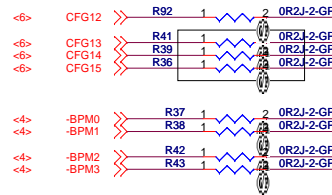
BOM1

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Size	Document Number		Rev
A2	Kendo-1-UMA		SB
Date:	Friday, March 13, 2009	Sheet 10 of	104

Please see Intel debug port Guidelines 385422

XDP Present function does not use.

Use these jumper, if pin count is overflow.



I2C Interface does not use.

<4> -XDP_PREQ
<4> -XDP_PRDY

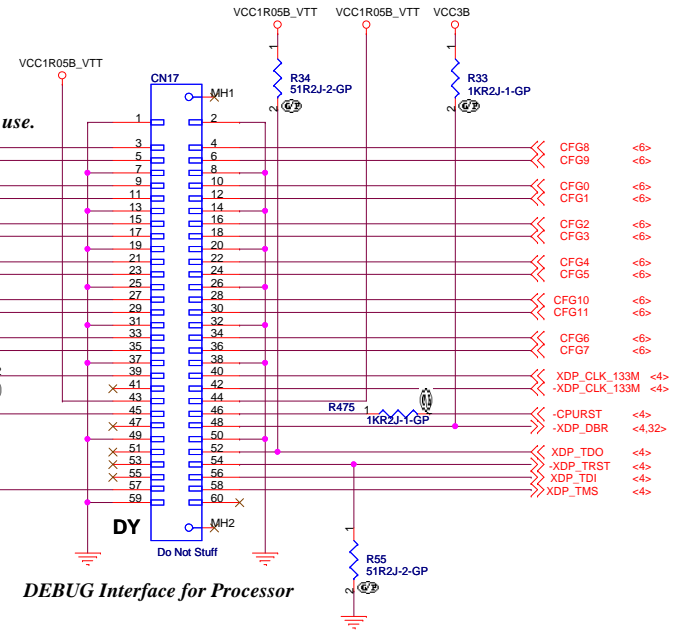
<6> CFG17
<6> CFG16

<4> -BPM4
<4> -BPM5

<4> -BPM6
<4> -BPM7

<4,36> CPUPWRGD
<4> TAPPWRG

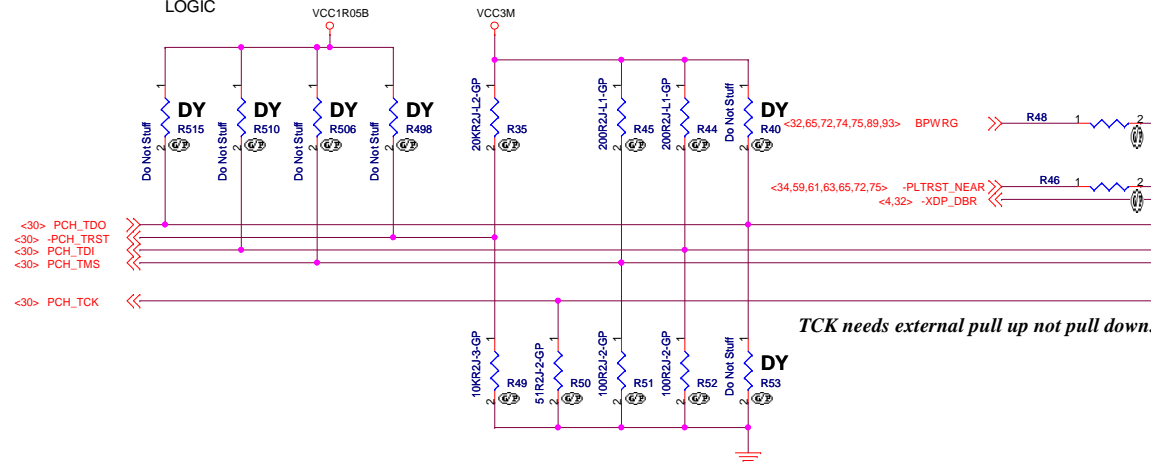
<4> XDP_TCK



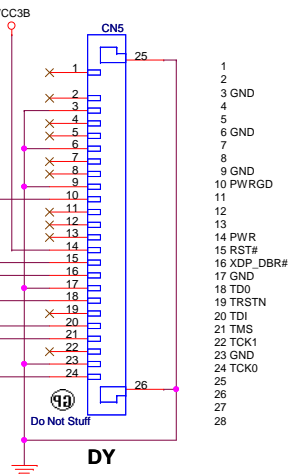
DEBUG Interface for Processor

PCH PIN	REF DES	PCH ES1 JTAG		PCH ES2 JTAG		MP	
		ENABLE	DISABLE	ENABLE	DISABLE	ENABLE	DISABLE
TDO	R40	NO_ASM	NO_ASM	200	NO_ASM	NO_ASM	NO_ASM
	R53	NO_ASM	NO_ASM	100	NO_ASM	NO_ASM	NO_ASM
	R515	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM
TMS	R37	200	NO_ASM	200	NO_ASM	NO_ASM	NO_ASM
	R51	100	NO_ASM	100	NO_ASM	NO_ASM	NO_ASM
	R510	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM
TDI	R38	200	20K	200	NO_ASM	NO_ASM	NO_ASM
	R52	100	10K	100	NO_ASM	NO_ASM	NO_ASM
	R506	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM
TCK	R50	51	51	51	51	51	51
TRST#	R35	20K	NO_ASM	20K	NO_ASM	NO_ASM	NO_ASM
	R49	10K	NO_ASM	10K	NO_ASM	NO_ASM	NO_ASM
	R498	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM

LOGIC



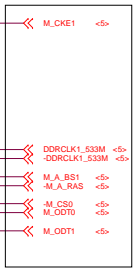
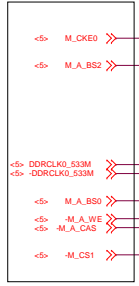
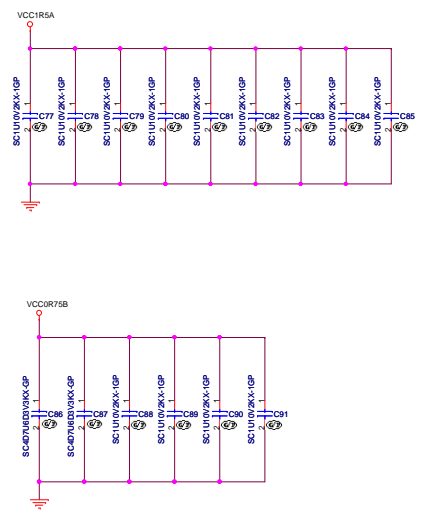
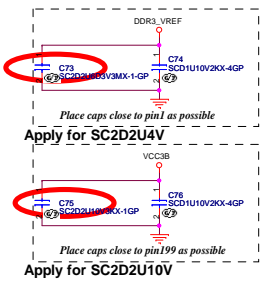
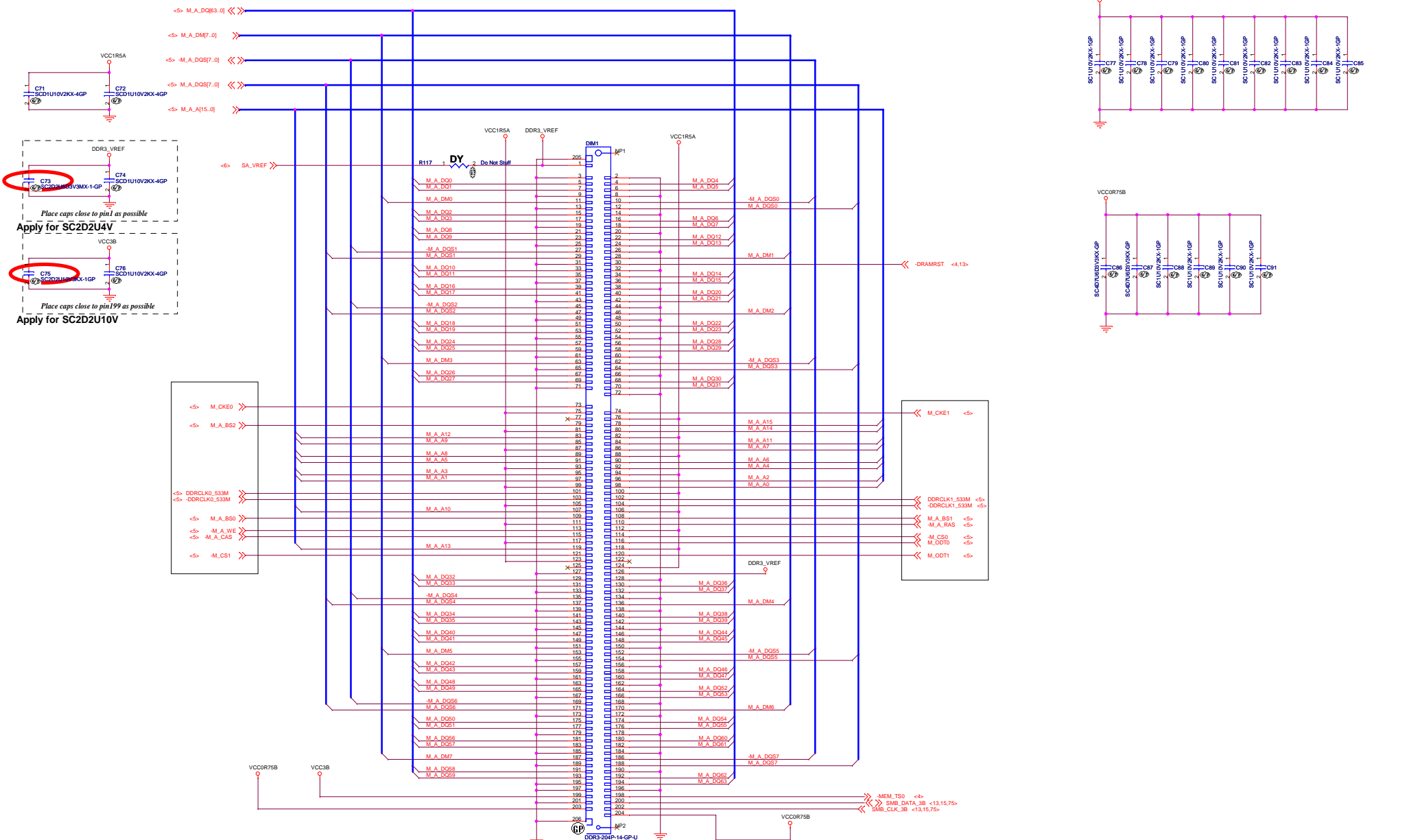
TCK needs external pull up not pull down.



24-PIN
DEBUG Interface for PCH

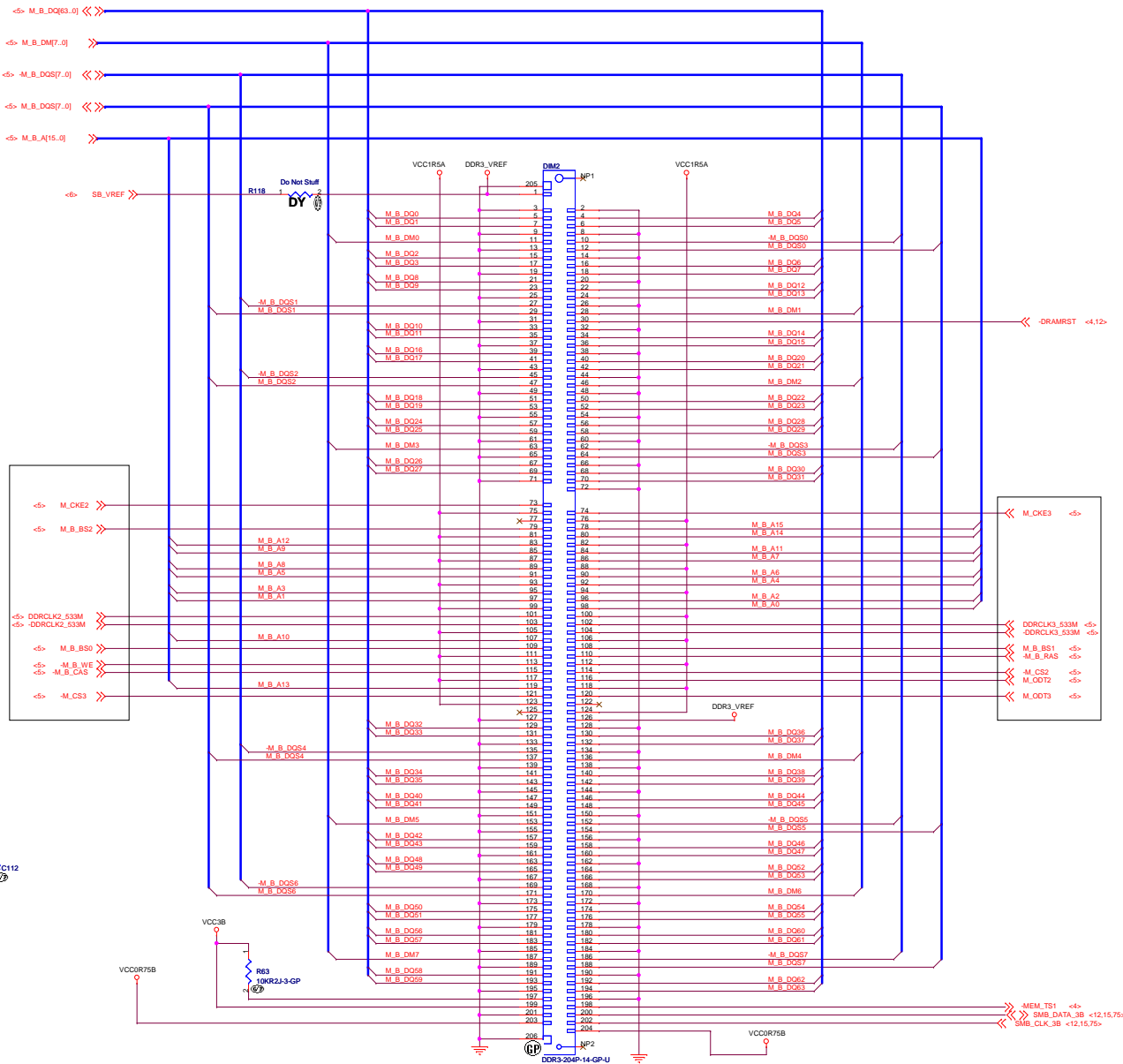
BOM1

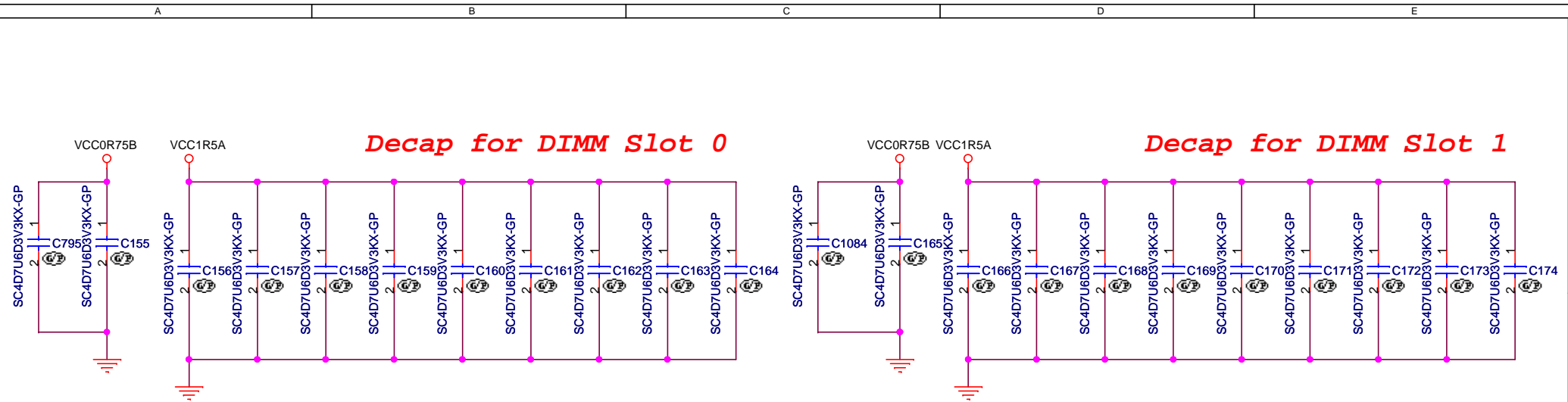
緯創資通		Wistron Corporation	
		21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title		XDP CONNECTOR	
Size A3	Document Number	Kendo-1-UMA	Rev SB
Date:	Friday, March 13, 2009	Sheet 11 of	104




SPD Address : 50h

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BOM1

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<i>DECAP For DIMMs</i>			
Size A4	Document Number Kendo-1-UMA		Rev SB
Date: Friday, March 13, 2009	Sheet	14 of	104

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BCM1

緯創資通		Wistron Corporation	
21F, 88, Sec.1, Hsin Tai W6 Rd., Hsichih,		Taipei Hsien 221, Taiwan, R.O.C.	
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Date: Friday, March 13, 2009		Sheet 16 of 104	

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BOM1	
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Title	
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Size C	Document Number
	Kendo-1-UMA
Date: Friday, March 13, 2009	Sheet 17 of 104
Rev.	SB

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BOM1	
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Title	
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Size C	Document Number
	Kendo-1-UMA
Date: Friday, March 13, 2009	Sheet 18 of 104
Rev.	SB

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BOM1

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Wistron Corporation

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Title

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Size
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Document Number

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Rev
SB

Date: Friday, March 13, 2009

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Title

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Size
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Document Number

Kendo-1-UMA

Rev
SB

Date: Friday, March 13, 2009

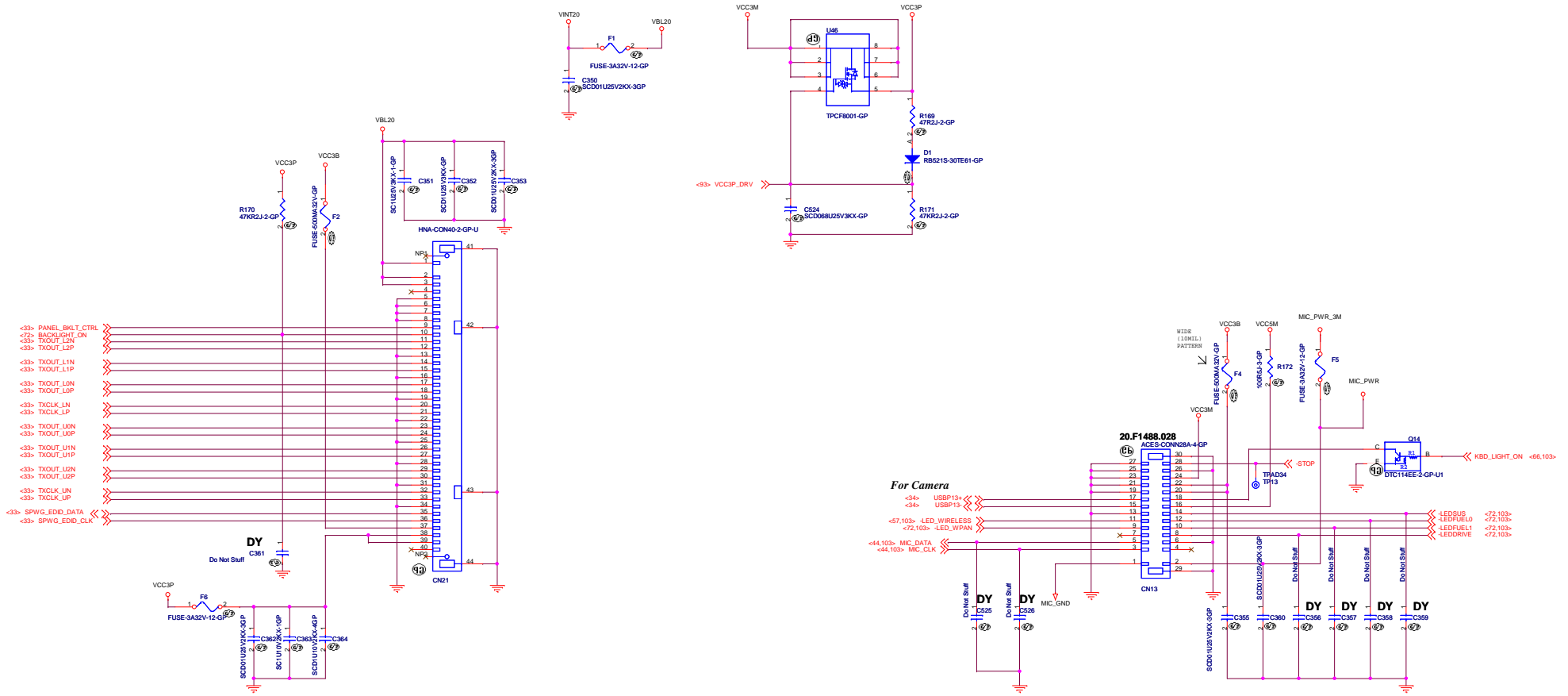
Sheet 22 of 104

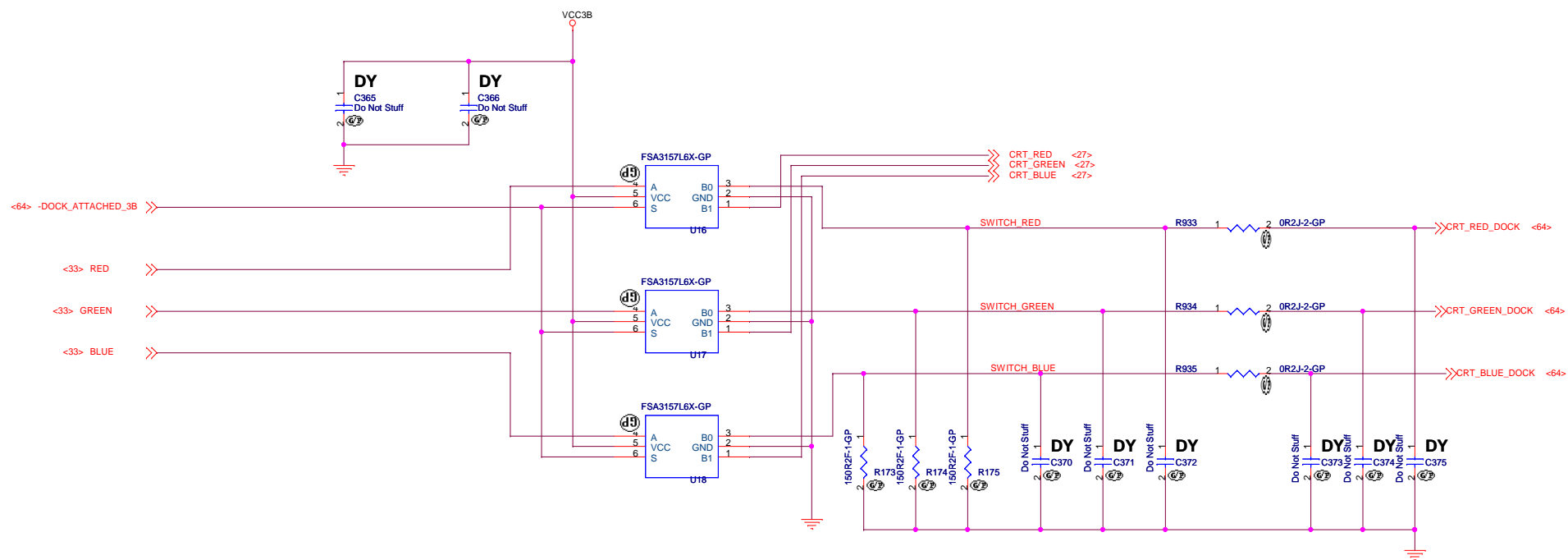
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LCD / Inverter Connector





BOM1

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Taipei Hsien 221, Taiwan, R.O.C.

Title

RGB SWITCH

Size
A3

Document Number

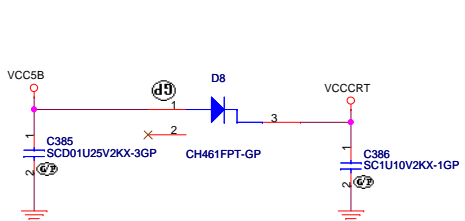
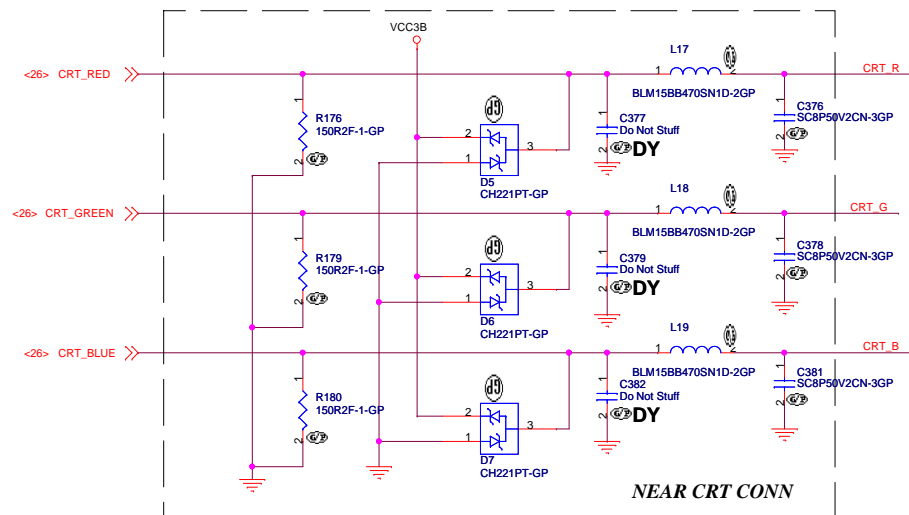
Kendo-1-UMA

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SB

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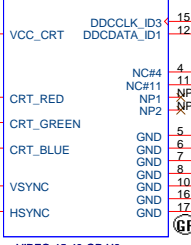
<https://Dr-Bios.com>



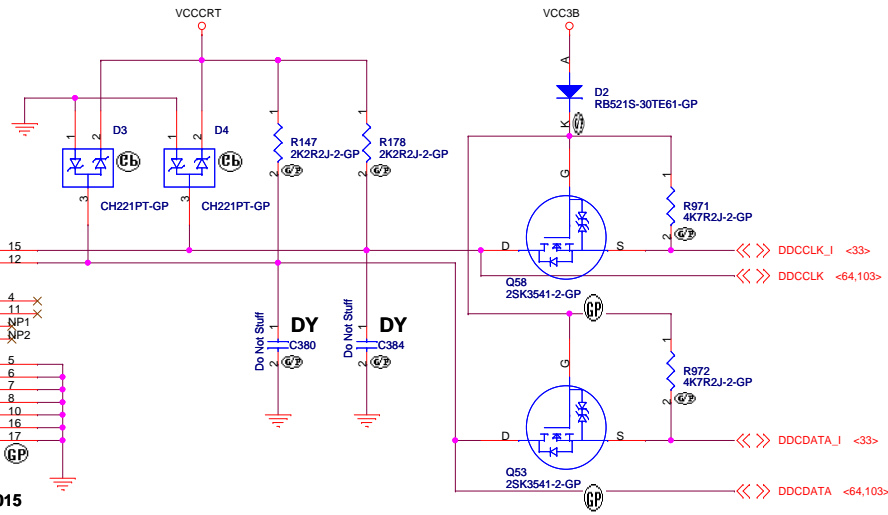
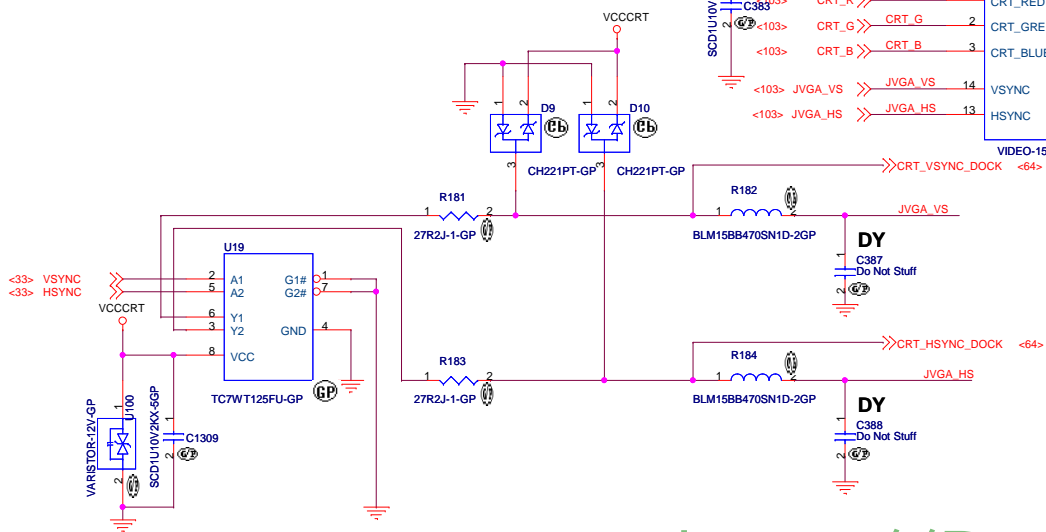
POLYSW 6V 1.5A NANOSMDC150F



CRT1 CRT CONN



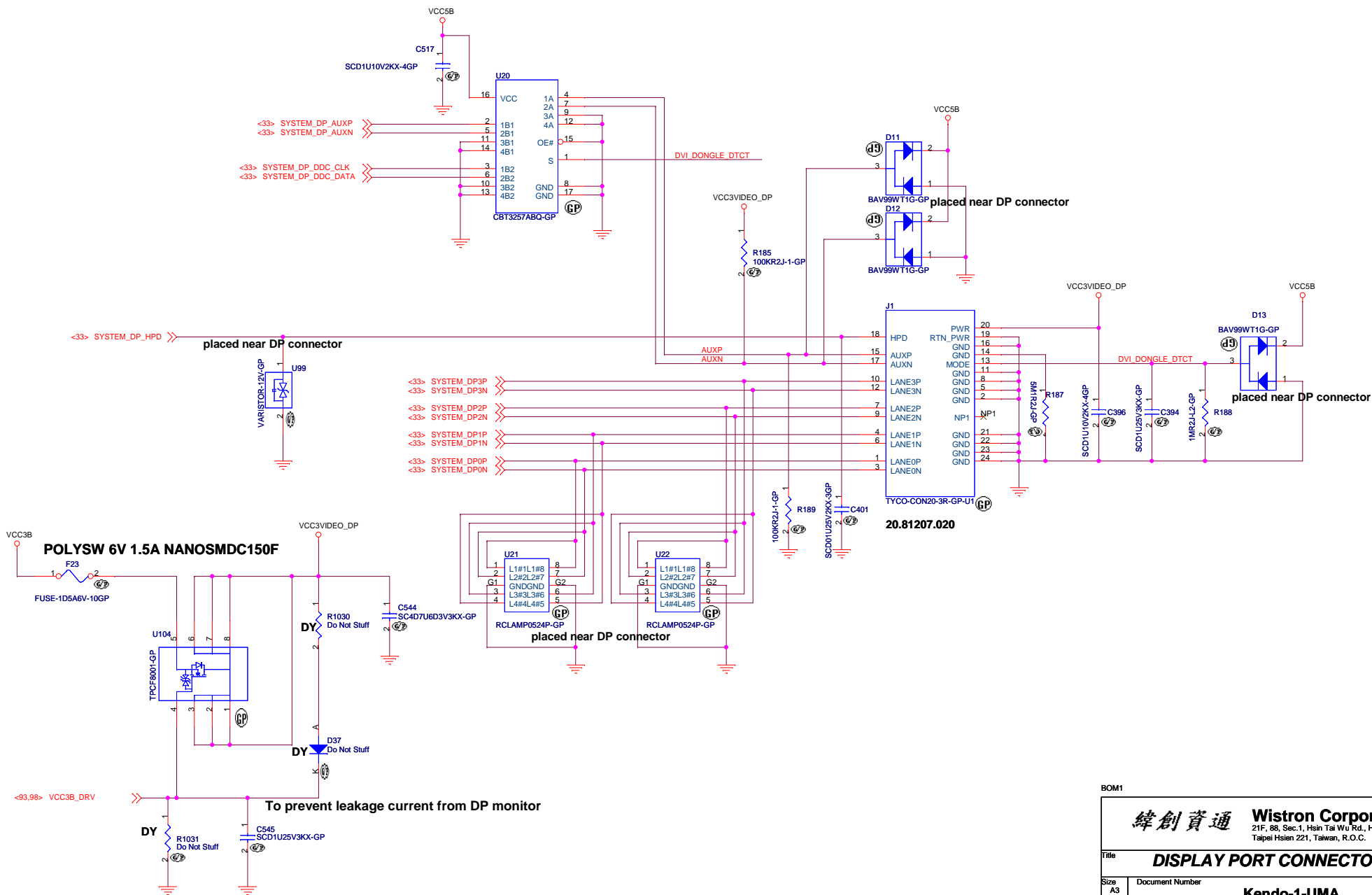
20.20394.015



To avoid leak current from a monitor.

BOM1

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
EXT CRT INTERFACE			
Size A3	Document Number Kendo-1-UMA	Rev SB	
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BOM1

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title **DISPLAY PORT CONNECTOR**

Size A3 Document Number **Kendo-1-UMA** Rev **SB**

Date: Friday, March 13, 2009 Sheet 28 of 104

<https://Dr-Bios.com>

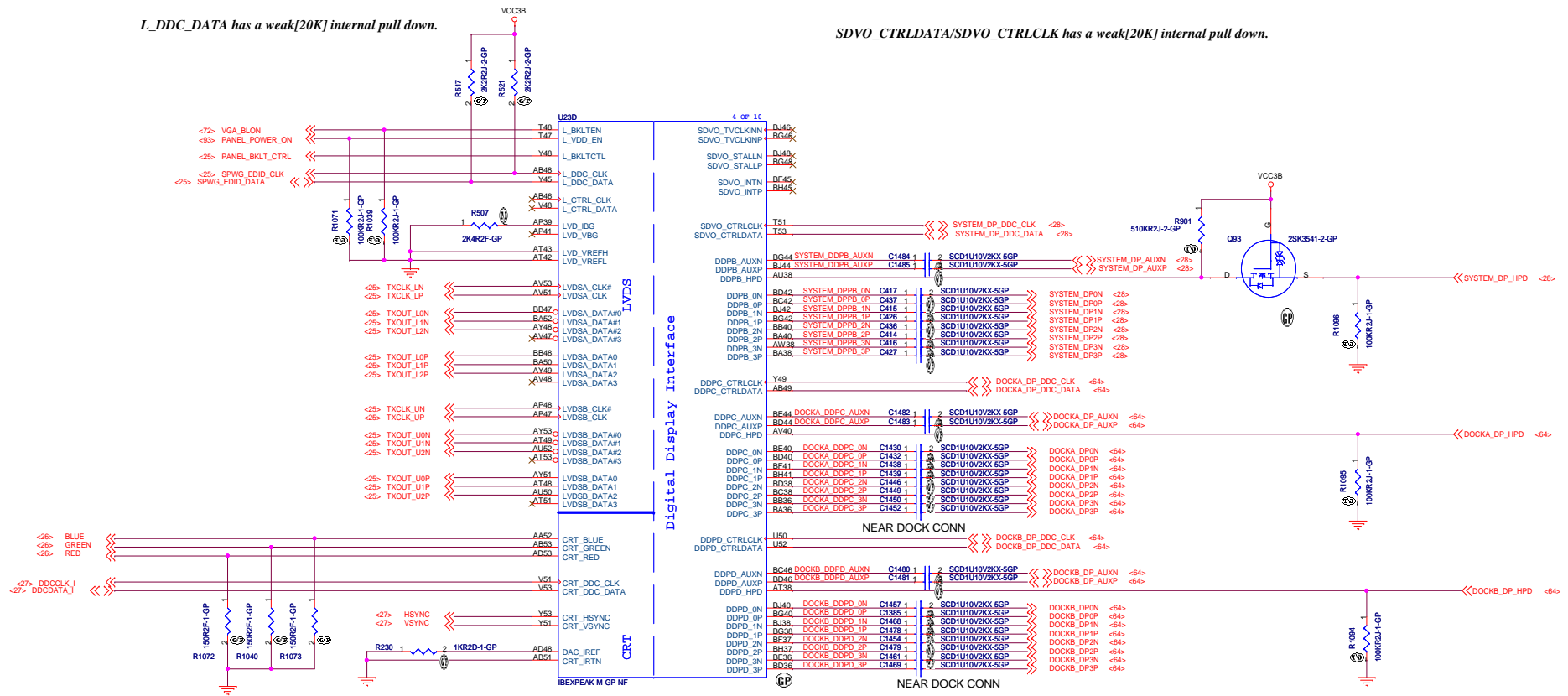
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BOM1

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
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Size A	Document Number		Rev
	Kendo-1-UMA		SB
Date:	Friday, March 13, 2009		Sheet 29 of 104

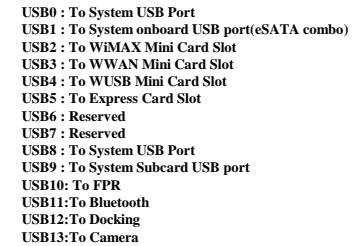
L_DDC_DATA has a weak[20K] internal pull down.

SDVO_CTRLDATA/SDVO_CTRLCLK has a weak[20K] internal pull down.

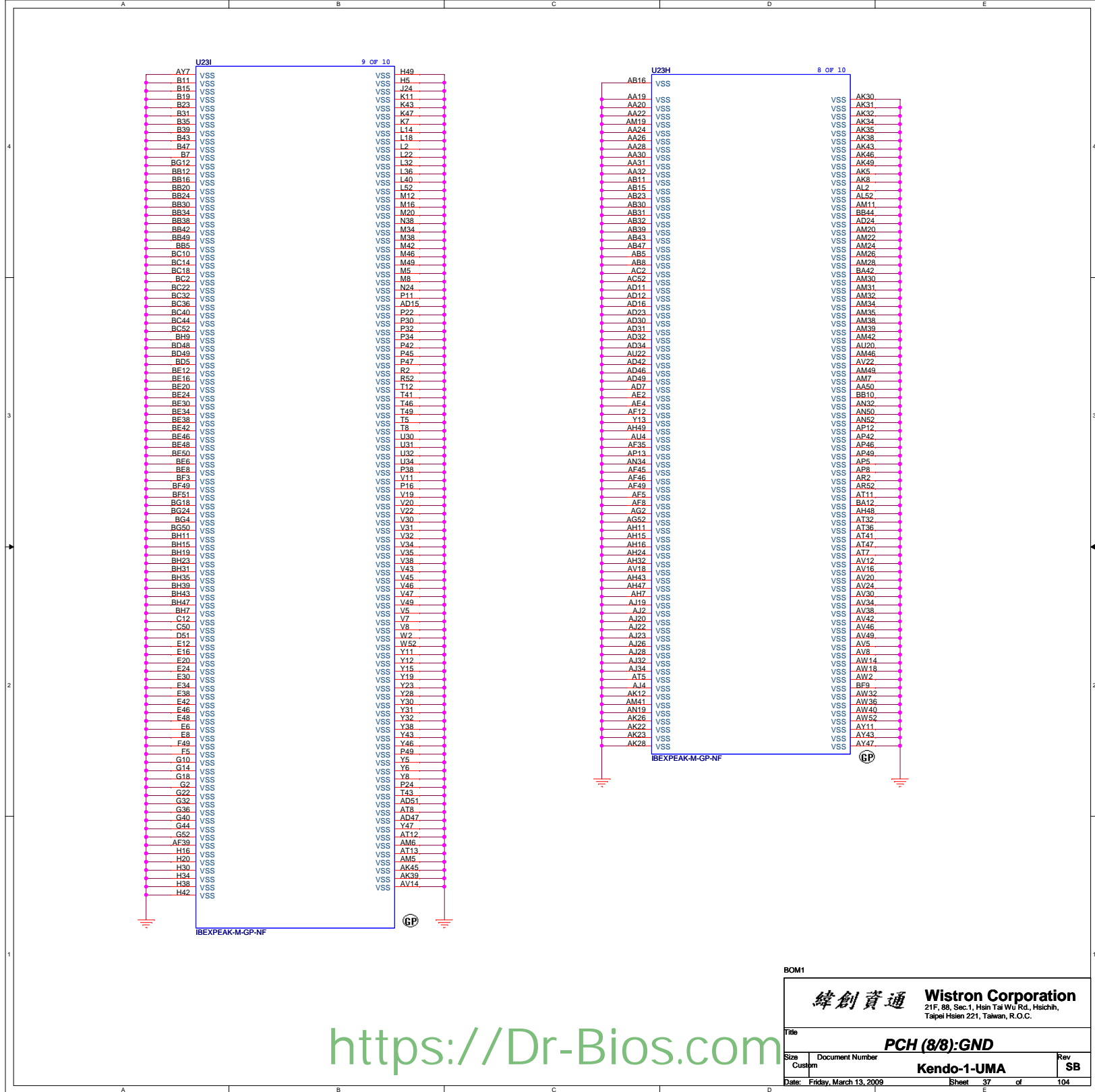


*DDP[D:C]_CTRLCLK has a weak[10K] internal pull down.
DDP[D:C]_CTRLDATA has a weak[20K] internal pull down.*

GNT0# has a weak[20K] internal pull up.
 GNT1#/GPIO15 has a weak[20K] internal pull up.
 To use SPI IF flash BIOS, GNT1#/GPIO15
 and GNT0# should not place external pull down.
 GNT2#/GPIO53 has a weak[20K] internal pull up.
 This pin should not have external pull down.
 GNT3#/GPIO55 has a weak[20K] internal pull up.
 If external pull down is applied,
 PCH will be "topblock swap" mode.



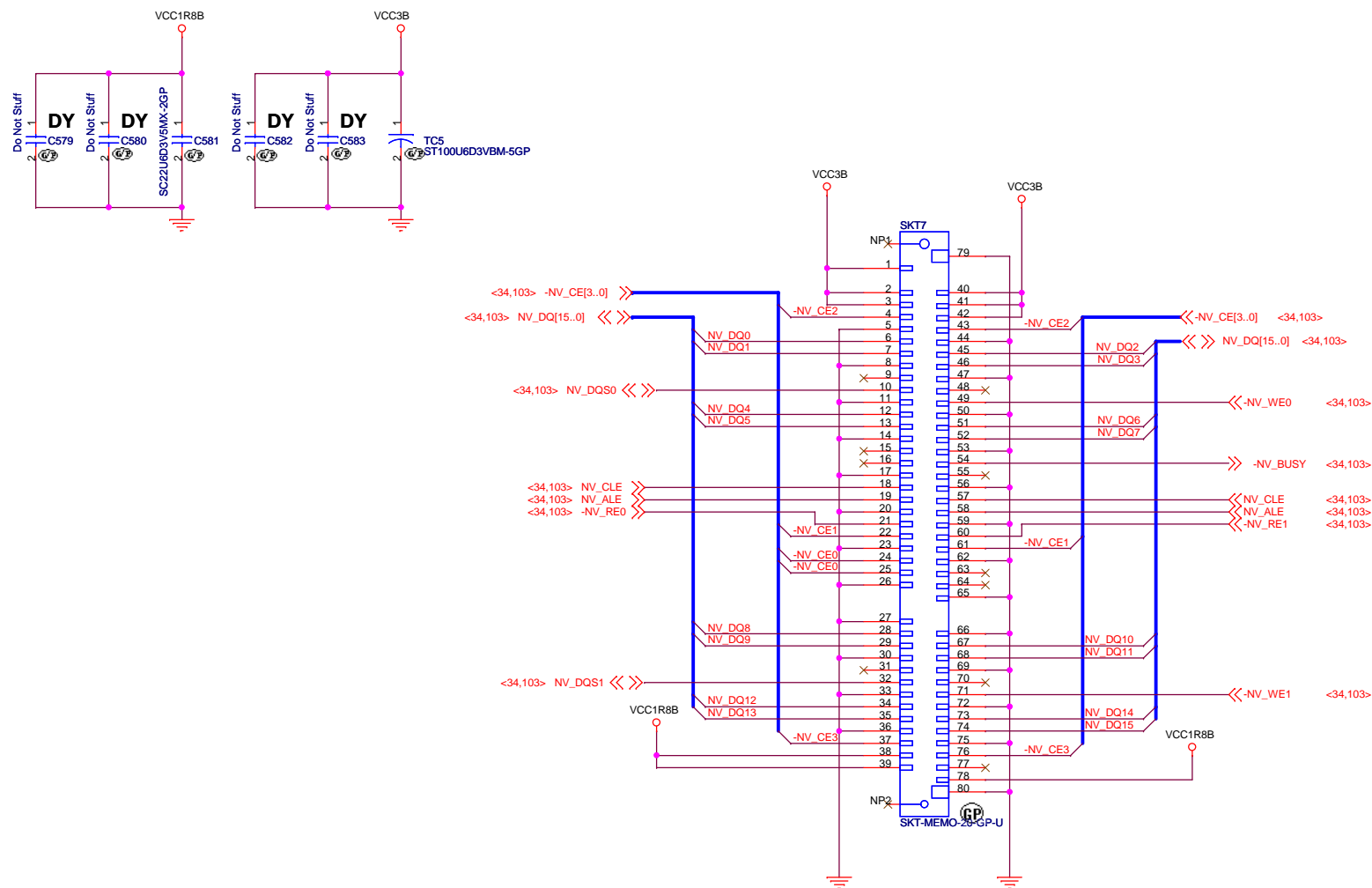
USBP 0	LEFT DUAL CONN	USB_ON1	-USB_PORT0_OC0
USBP 1	LEFT COMBO CONN	USB_ON1	-USB_PORT1_OC1
USBP 8	LEFT DUAL CONN	USB_ON1	-USB_PORT8_OC4
USBP 9	RIGHT SUB CARD	USB_ON2	-USB_PORT9_OC5



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BOM1

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
PCH (8/8):GND			
Size	Document Number	Rev	
Custom	Kendo-1-UMA	SB	
Date:	Friday, March 13, 2009	Sheet	37 of 104



BOM1

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title		
TURBO MEMORY SLOT		
Size	Document Number	Rev
B	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 38 of 104

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BOM1

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

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Size
A

Document Number

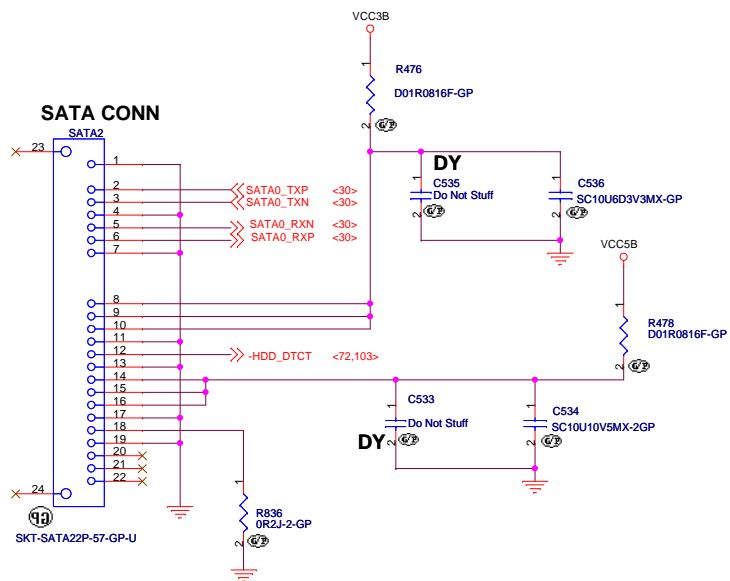
Kendo-1-UMA

Rev
SB

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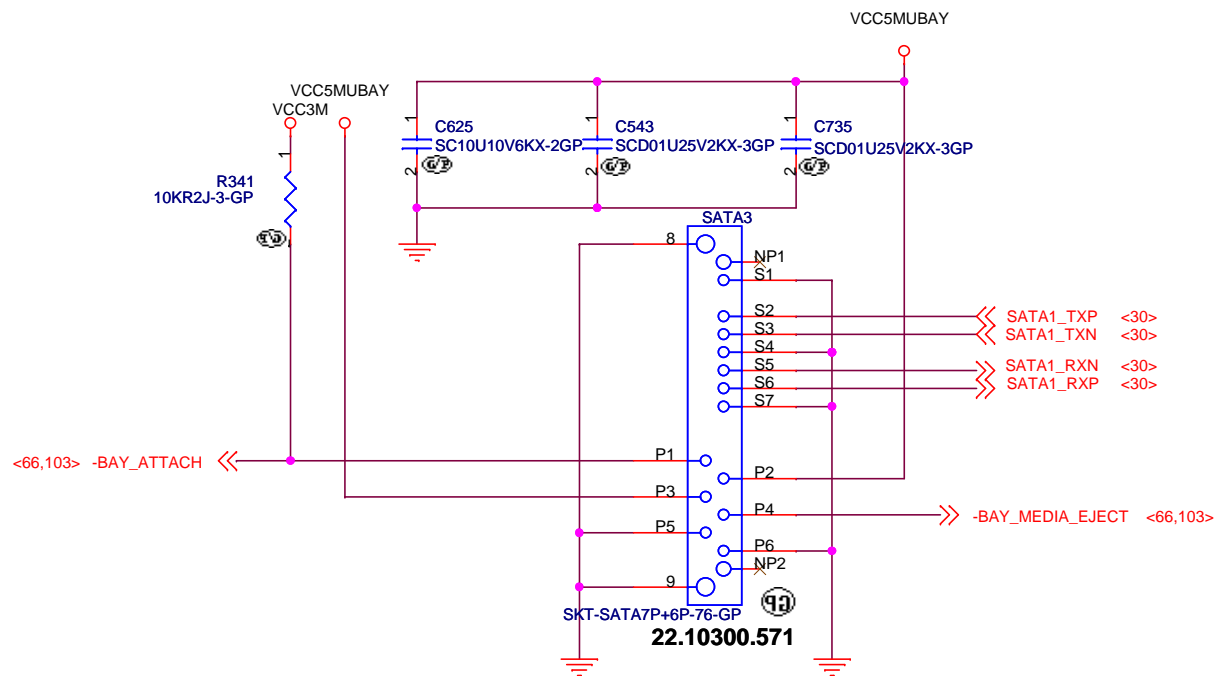
<https://Dr-Bios.com>




BOM1

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
SATA HDD CONN			
Size A3	Document Number	Rev SB	
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BOM1

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
SATA ODD BAY			
Size A4	Document Number Kendo-1-UMA		Rev SB
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BOM1

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

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Size
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Document Number

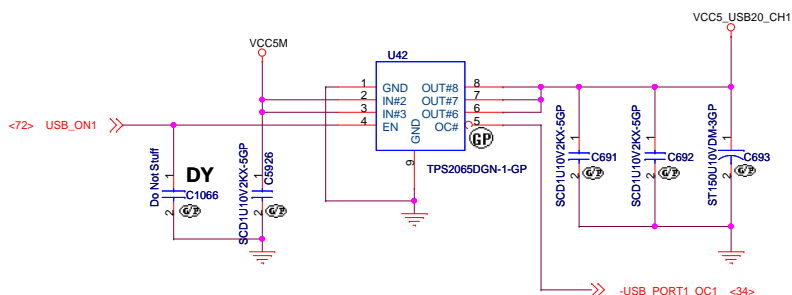
Kendo-1-UMA

Rev
SB

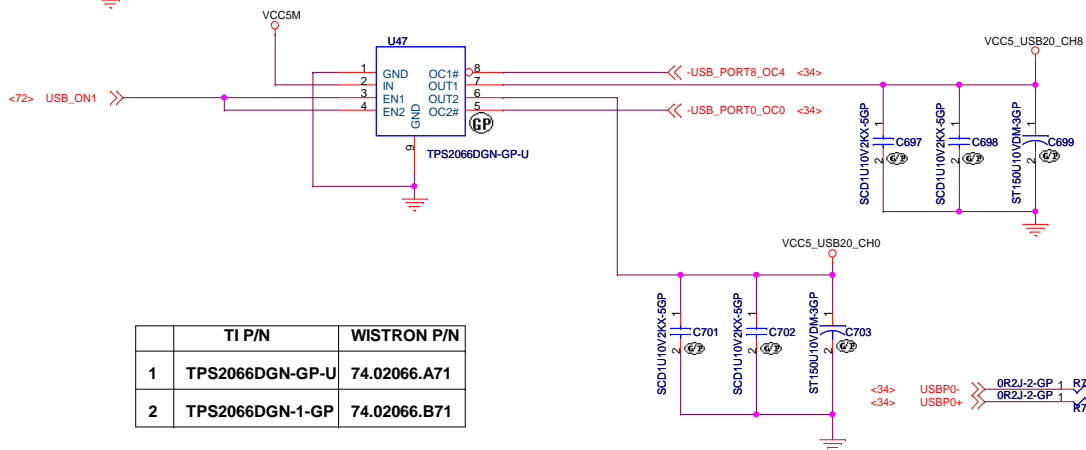
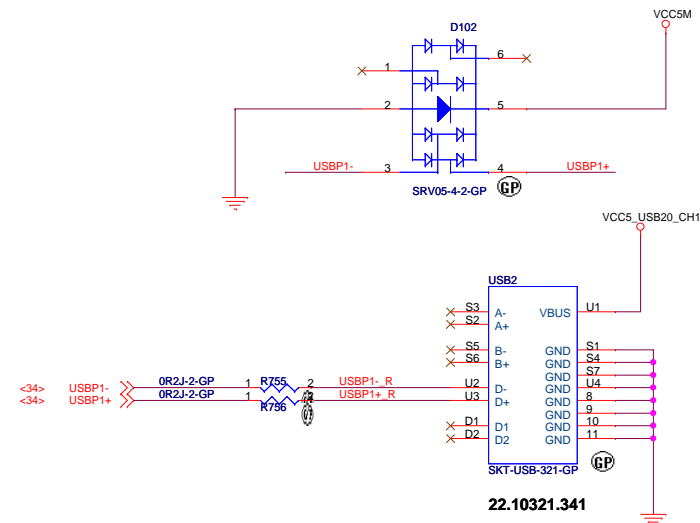
Date: Friday, March 13, 2009

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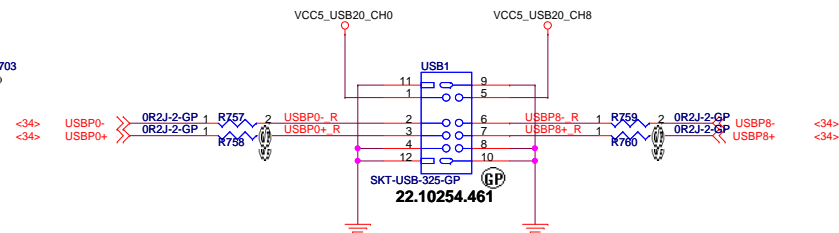
<https://Dr-Bios.com>



	TI P/N	WISTRON P/N
1	TPS2065DGN-GP	41A1229AA
2	TPS2065DGN-1-GP	74.02065.A79
3	BD8010FVJ-GP	74.08010.07G



	TI P/N	WISTRON P/N
1	TPS2066DGN-GP-U	74.02066.A71
2	TPS2066DGN-1-GP	74.02066.B71



BOM1

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
USB2.0 CONN&PWR		
Title Size A3	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 43 of 104	

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BOM1

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

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Size
A

Document Number

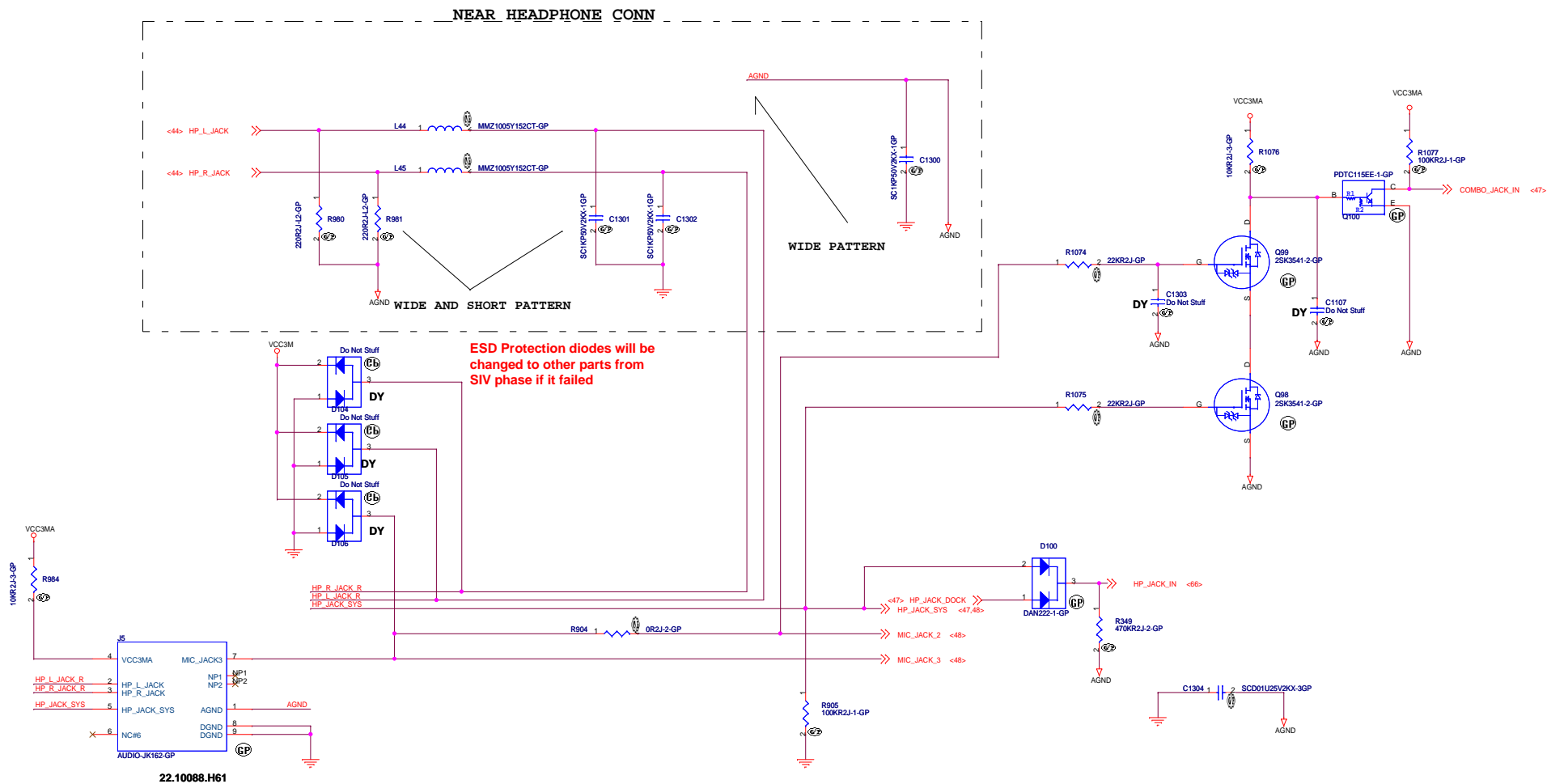
Kendo-1-UMA

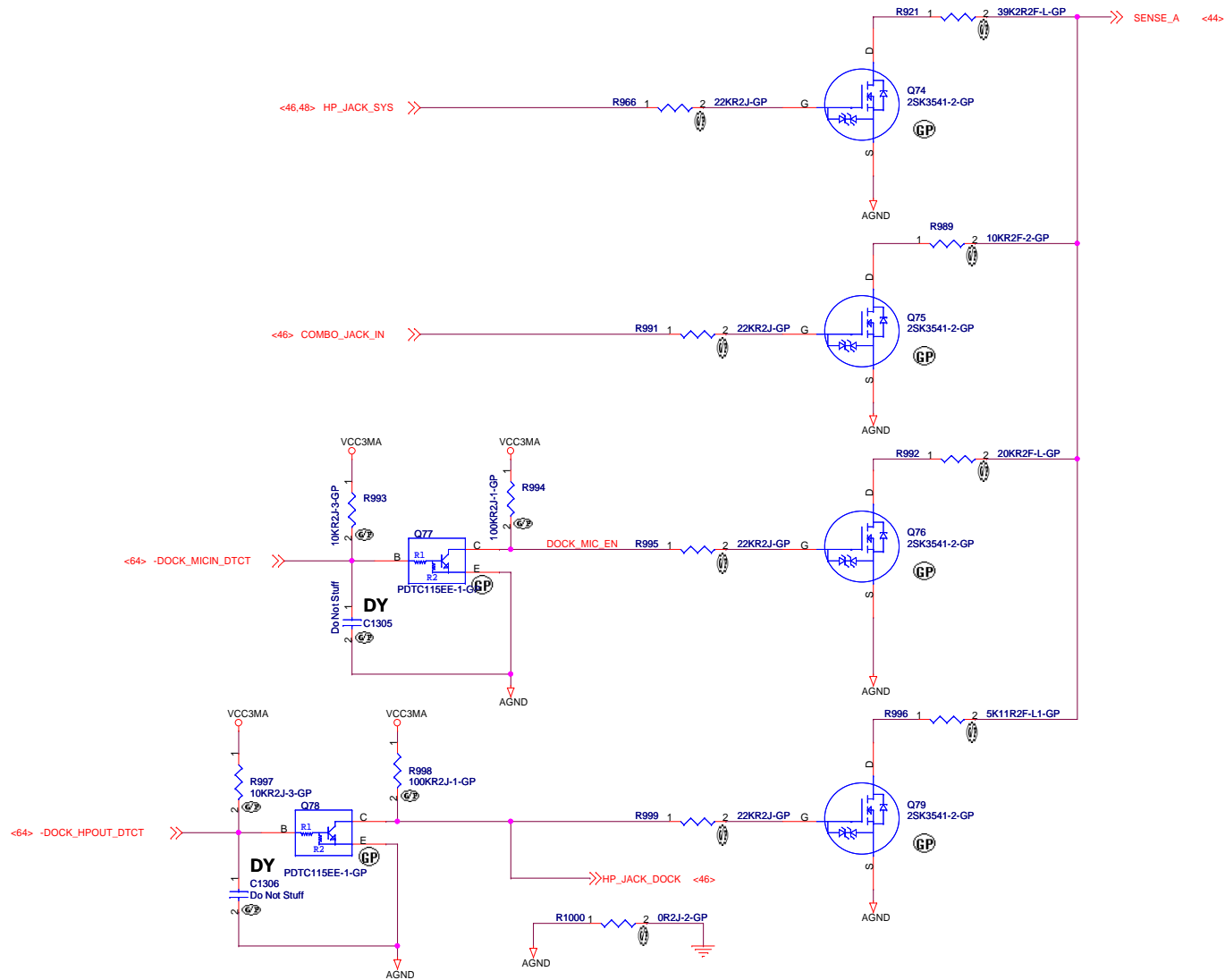
Rev
SB

Date: Friday, March 13, 2009

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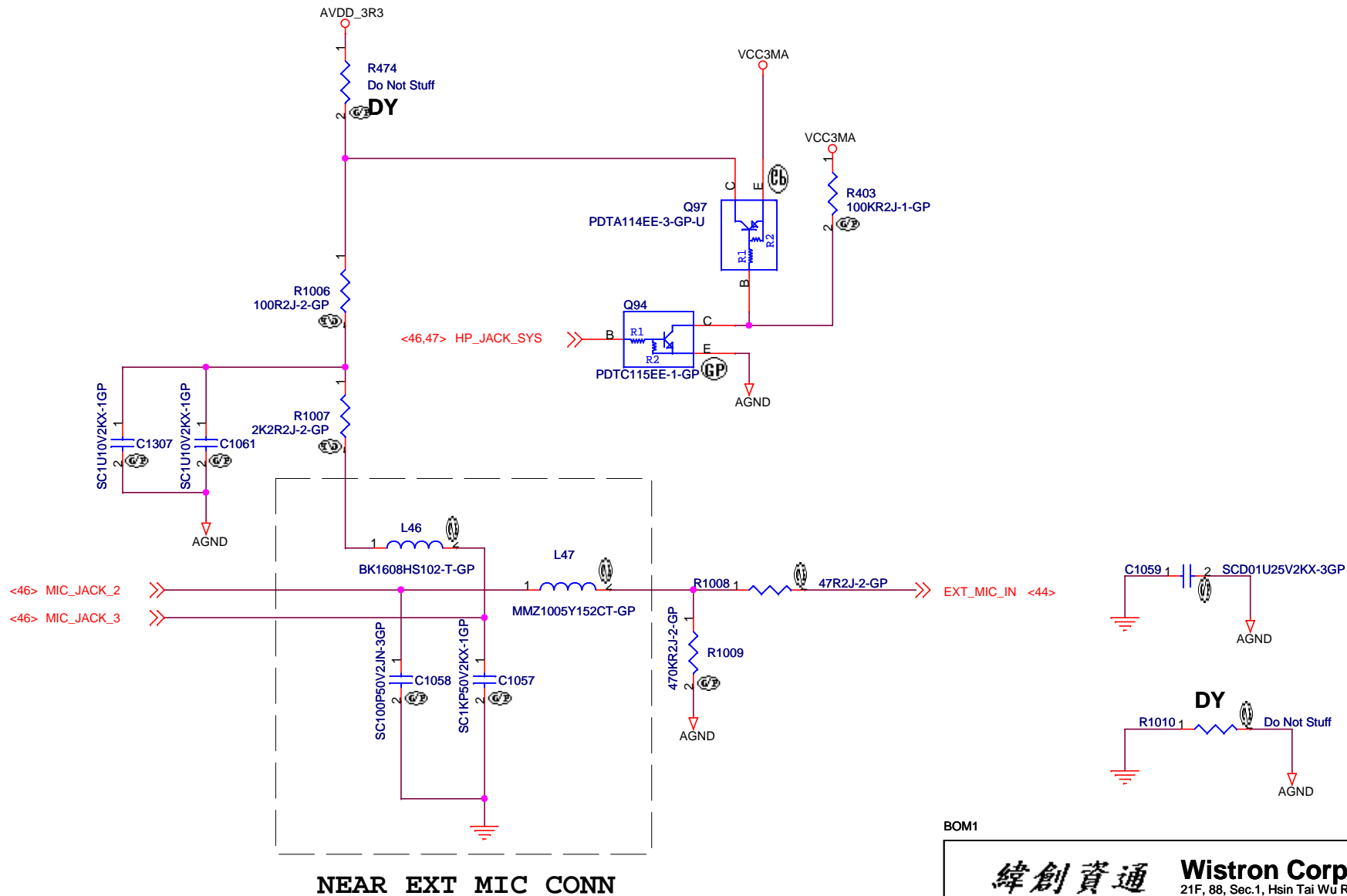
<https://Dr-Bios.com>





BOM1

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichin, Taipei Hsien 221, Taiwan, R.O.C.	
AUDIO JACK SENSE	
Size A3	Document Number Kendo-1-UMA
Date: Friday, March 13, 2009	Sheet 47 of 104
Rev SB	



BOM1

<p>緯創資通 Wistron Corporation</p> <p>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</p>	
<p>Title</p> <p>AUDIO EXT MIC I/F</p>	
<p>Size</p> <p>A4</p>	<p>Document Number</p> <p>Kendo-1-UMA</p>
<p>Date: Friday, March 13, 2009</p>	<p>Rev</p> <p>SB</p>
<p>Sheet</p> <p>48</p>	<p>of</p> <p>104</p>

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BOM1

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Title

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A

B

C

D

E

4

4

3

3

2

2

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1

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BOM1

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Taipei Hsien 221, Taiwan, R.O.C.

Title

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A

Document Number

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<https://Dr-Bios.com>

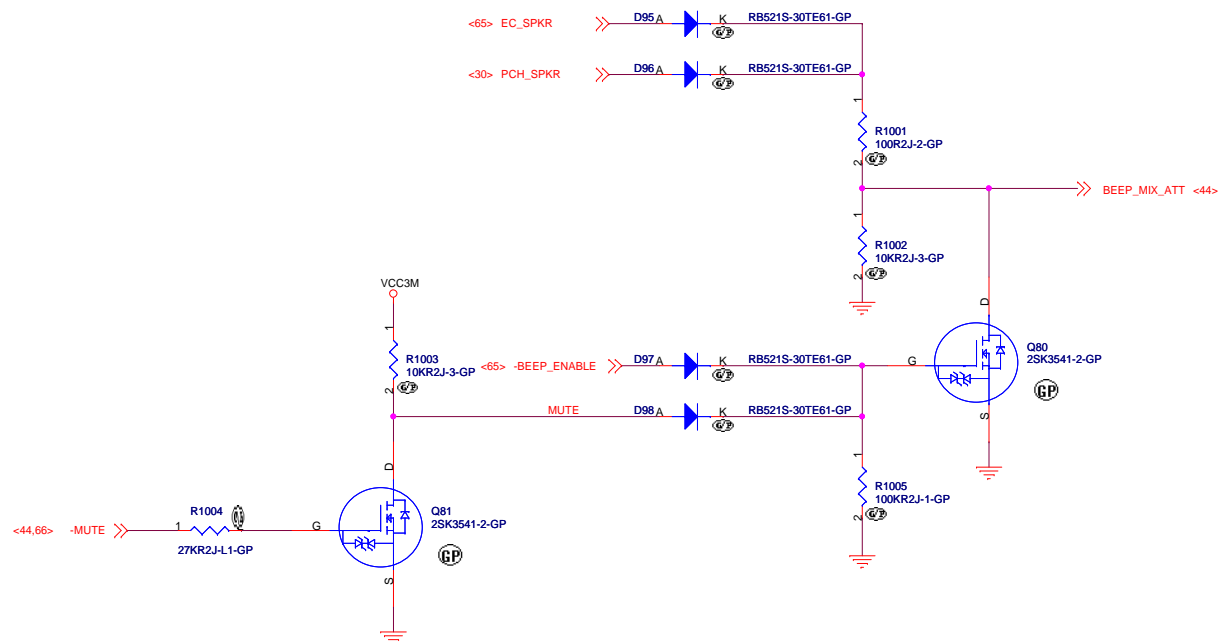
A

B

C

D

E



BOM1

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title BEEP CONTROL	
Size A3	Document Number Kendo-1-UMA
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Title

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Size
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Document Number

Kendo-1-UMA

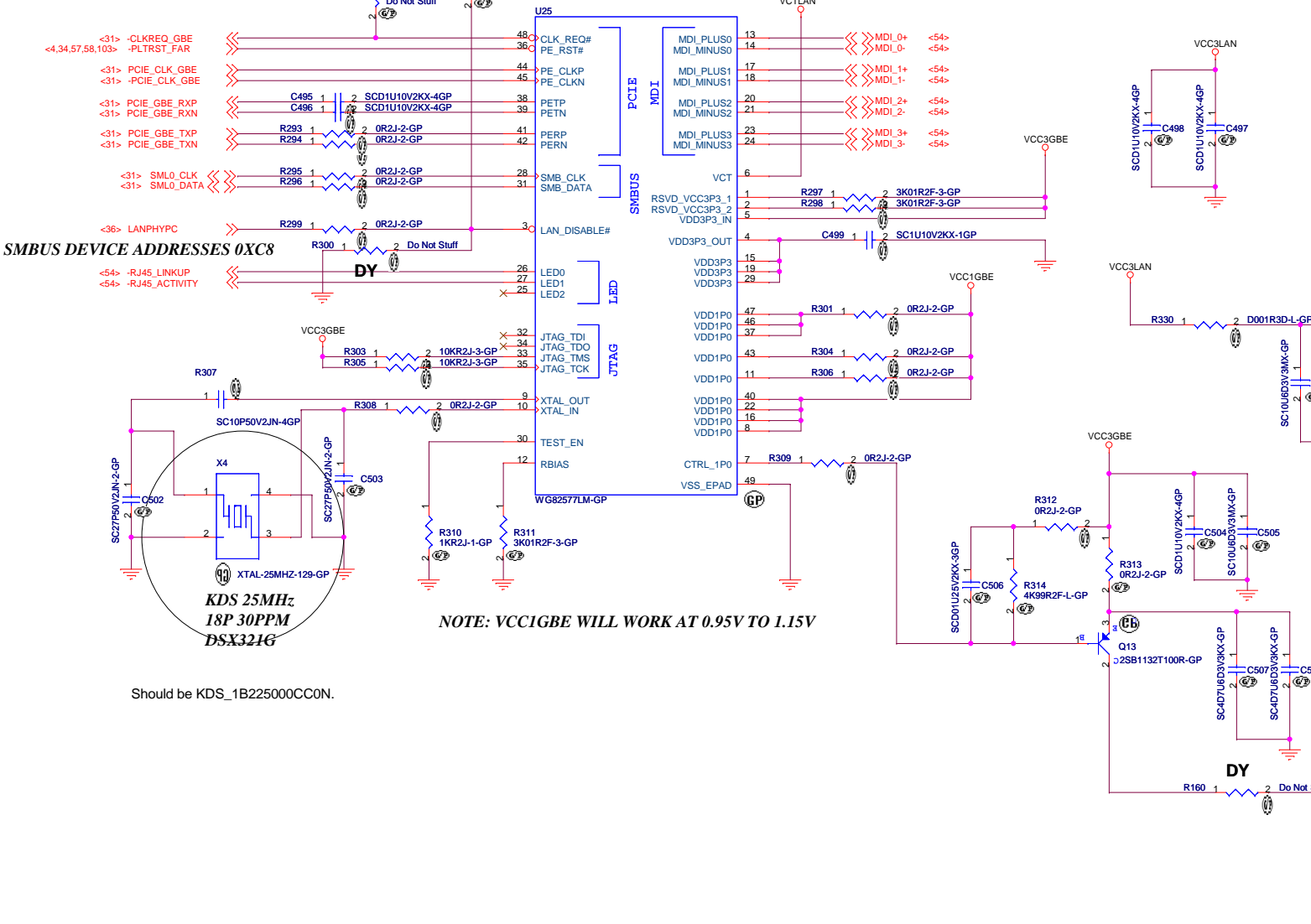
Rev
SB

Date: Friday, March 13, 2009

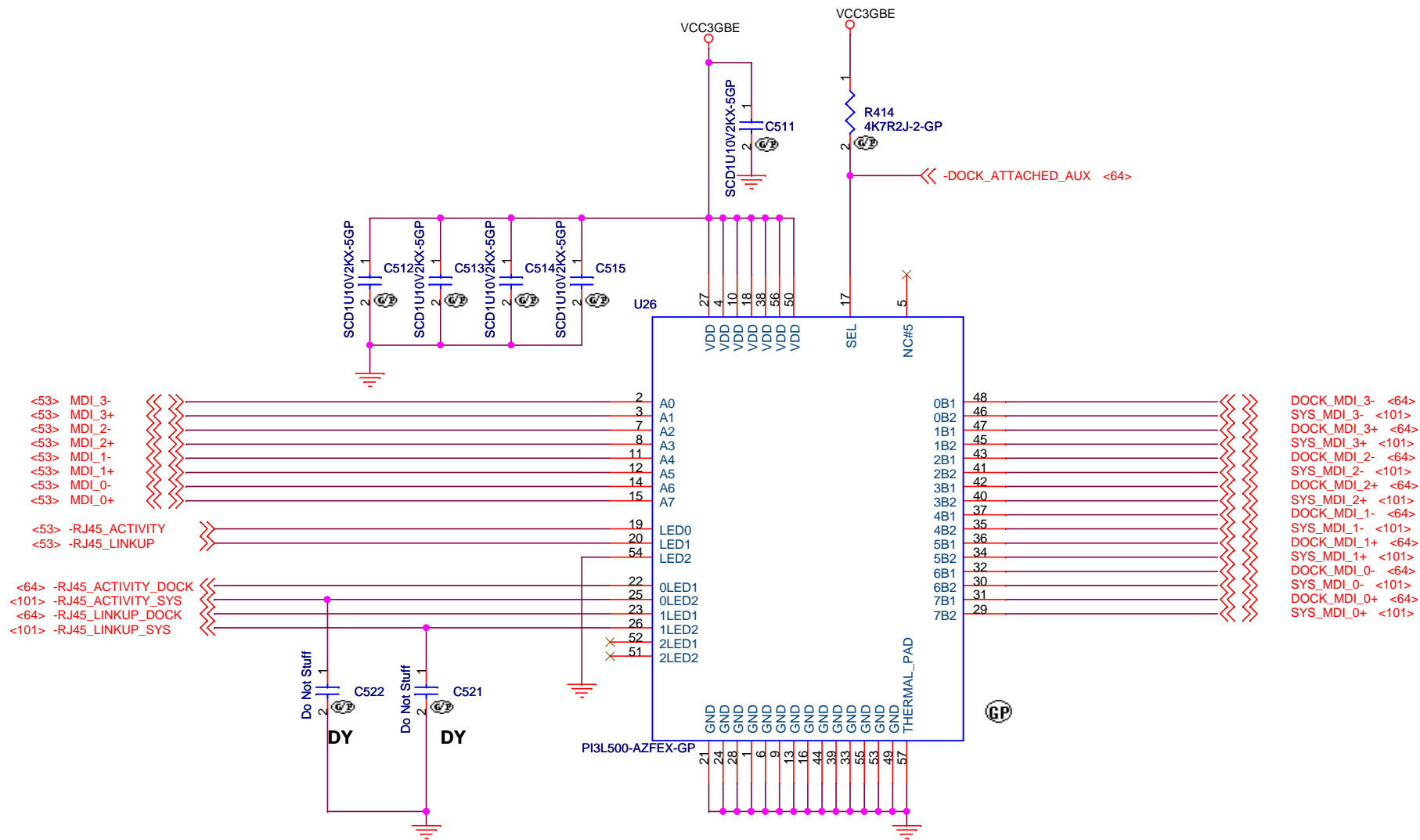
Sheet 52 of 104

<https://Dr-Bios.com>

PU is placed on PCH page



BOM1			
緯創資通 Wistron Corporation			
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title GBE HANKSVILLE			
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[Source Caditate]				
1st	Pericom	PI3L500AZFEX	73.3L500.003	41R0539AA
2nd	TI	TS3L500AERHUR	73.3L500.A0V	41R0539BA

BOM1

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
GBE LAN SW			
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BOM1

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Document Number

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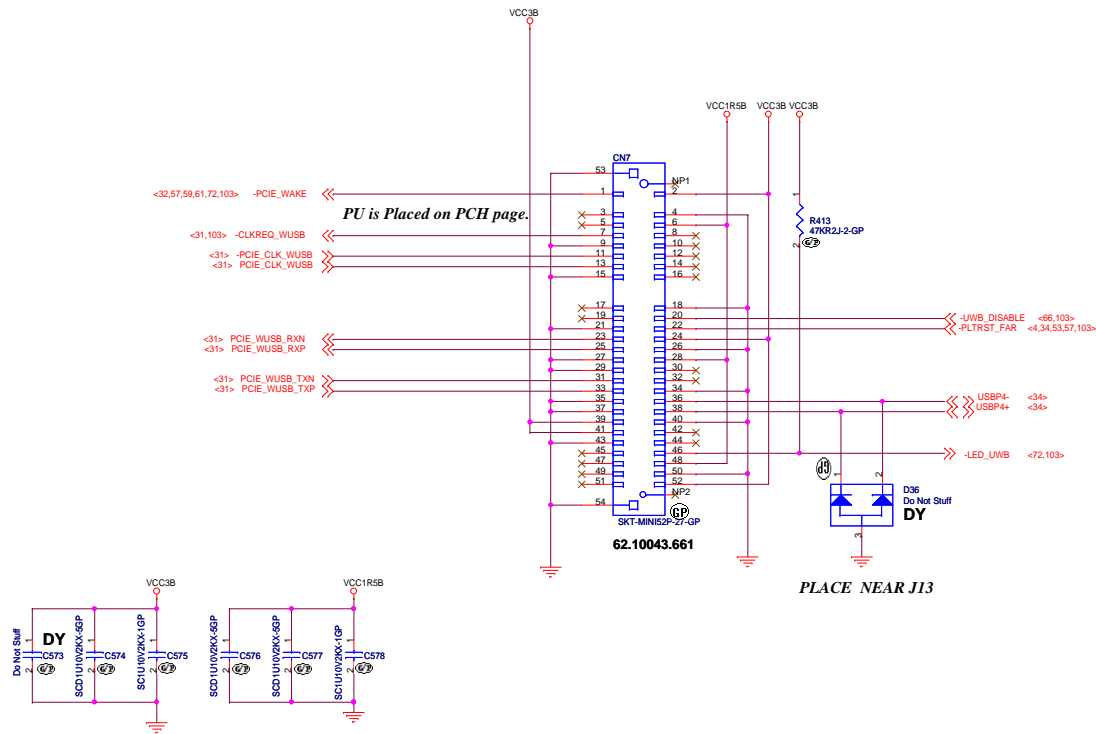
Date: Friday, March 13, 2009

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<https://Dr-Bios.com>

HALF MINI CARD FOR WUSB

7.0H CONNECTOR



<https://Dr-Bios.com>

PU is placed on PCH page.

<31> -CLKREQ_MCC

<32,57,58,61,72,103> -PCIE_WAKE

<31> PCIE_CLK_MCC

<31> -PCIE_CLK_MCC

<31> PCIE_MCC_RXP

<31> PCIE_MCC_RXN

<31> PCIE_MCC_TXP

<31> PCIE_MCC_TXN

<11,34,61,63,65,72,75> -PLTRST_NEAR

R5U230 package should be used new 6mm x6mm
wait wistron apply new symbol

Richo Recommended Conditions:
Normal Frequency: 24.576MHz
Frequency Tolerance: +/- 50ppm.
Load Capacitance: 10pF.

KDS Recommended Conditions:
Normal Frequency: 24.576MHz
Frequency Tolerance: +/- 30ppm.
Load Capacitance: 10pF.
DSX321G XTAL-24D576MHZ-5-GP

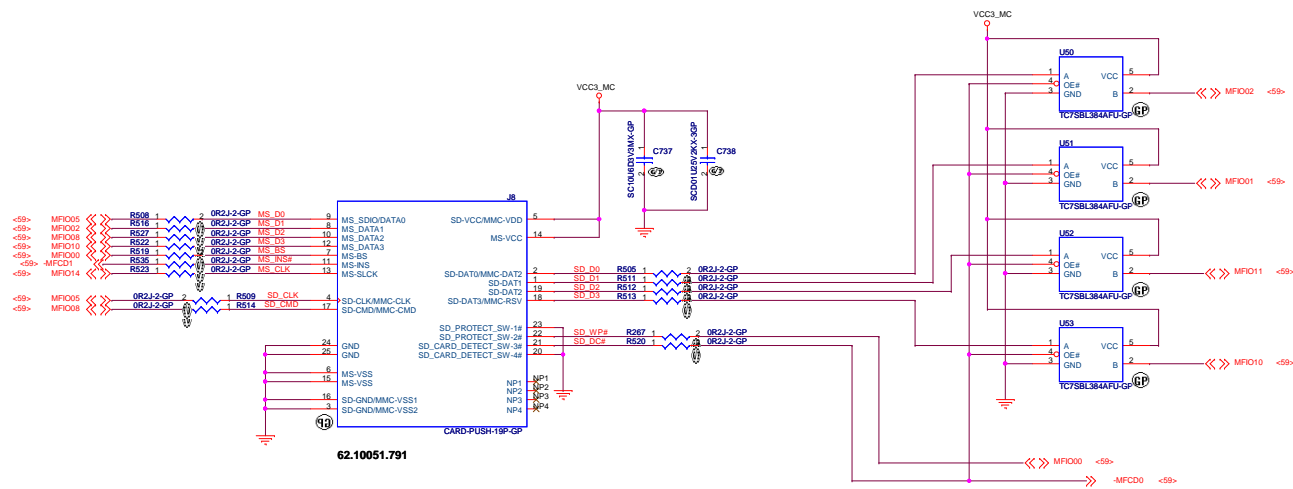
MEDIA I/F	SD/MMC	MEMORYSTICK	XD
MFIO00	SDWP#	MSBS	XD_D7
MFIO01	SD_D1		XD_D6
MFIO02	SD_D0	MS_D1	XD_D5
MFIO03	(SD_D7)		XD_D4
MFIO04	(SD_D6)	(MS_D5)	XD_D3
MFIO05	SD_CLK	MSD0	XD_D2
MFIO06			XD_D1
MFIO07	(SD_D5)	(MS_D4)	XD_D0
MFIO08	SD_CDM	MS_D2	XD_WP#
MFIO09	(SD_D4)	(MS_D6)	XD_WE#
MFIO10	SD_D3	MS_D3	XD_ALE
MFIO11	SD_D2		XD_CLE
MFIO12		(MS_D7)	XD_CE#
MFIO13		MS_CLK	XD_RE#
MFIO14			XD_R/B
MFCD0#	SDDC#		XDCD0#
MFCD1#		MSINS#	XDCD1#

BOM1

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
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Title	1394/MEDIA CARD CONTROLLER		
Size	Document Number	Rev	
A3		SB	
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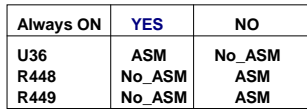
<https://Dr-Bios.com>



BCM1

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai W6 Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

MEDIA CARD INTERFACE		
Doc A2	Document Number	Rev SB
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LOGIC

USB_SEL_ALWAYS_ON	USB_EXC
L	USBP5
H	USBAO

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BOM1

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Taipei Hsien 221, Taiwan, R.O.C.

Title

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Size
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Document Number

Kendo-1-UMA

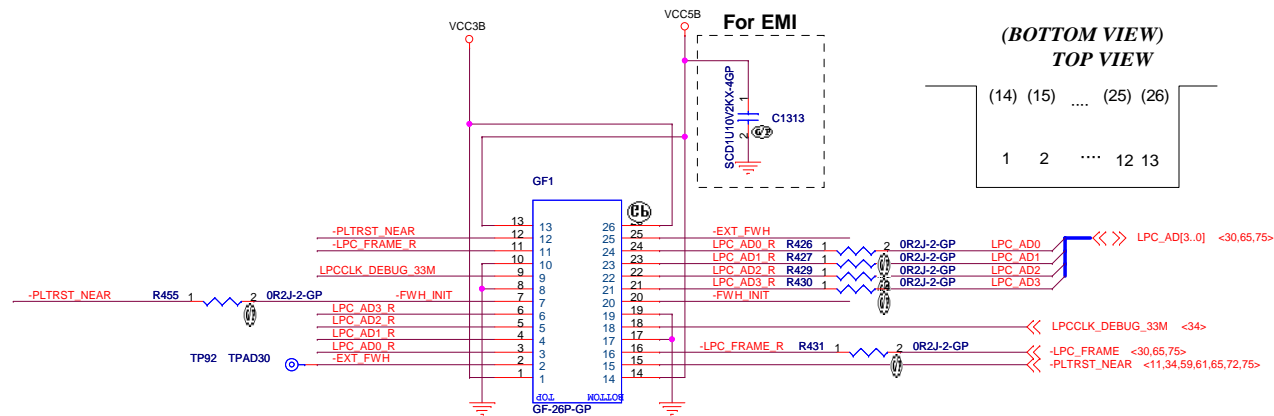
Rev
SB

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<https://Dr-Bios.com>

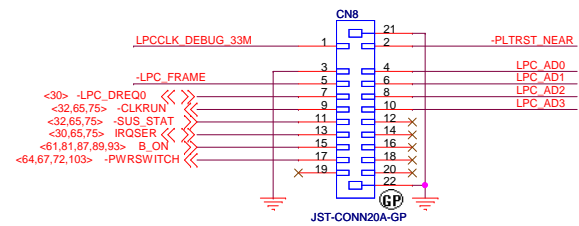
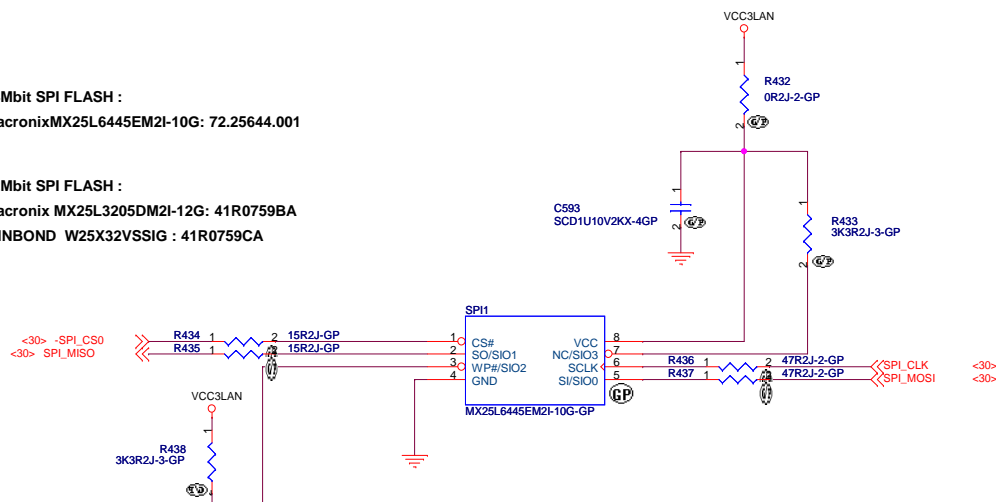
Golden Finger for Debug Board



Lenovo Debug Tool IF.

64Mbit SPI FLASH :
MacronixMX25L6445EM2I-10G: 72.25644.001

32Mbit SPI FLASH :
Macronix MX25L3205DM2I-12G: 41R0759BA
WINBOND W25X32VSSIG : 41R0759CA



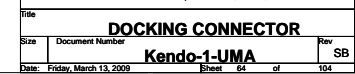
BOM1

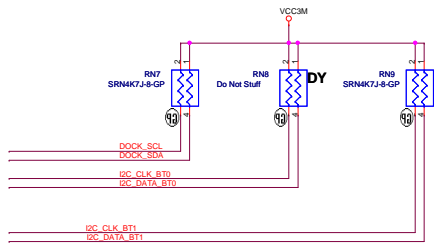
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title **SPI & LPC DEBUG CARD**

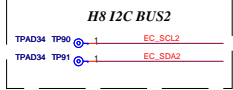
Size A3 Document Number **Kendo-1-UMA** Rev SB

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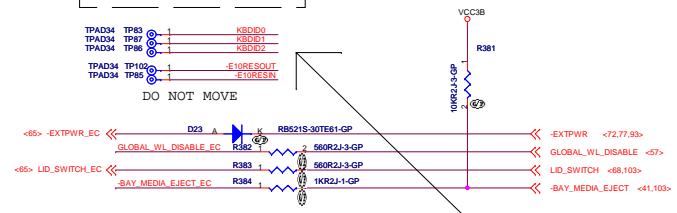




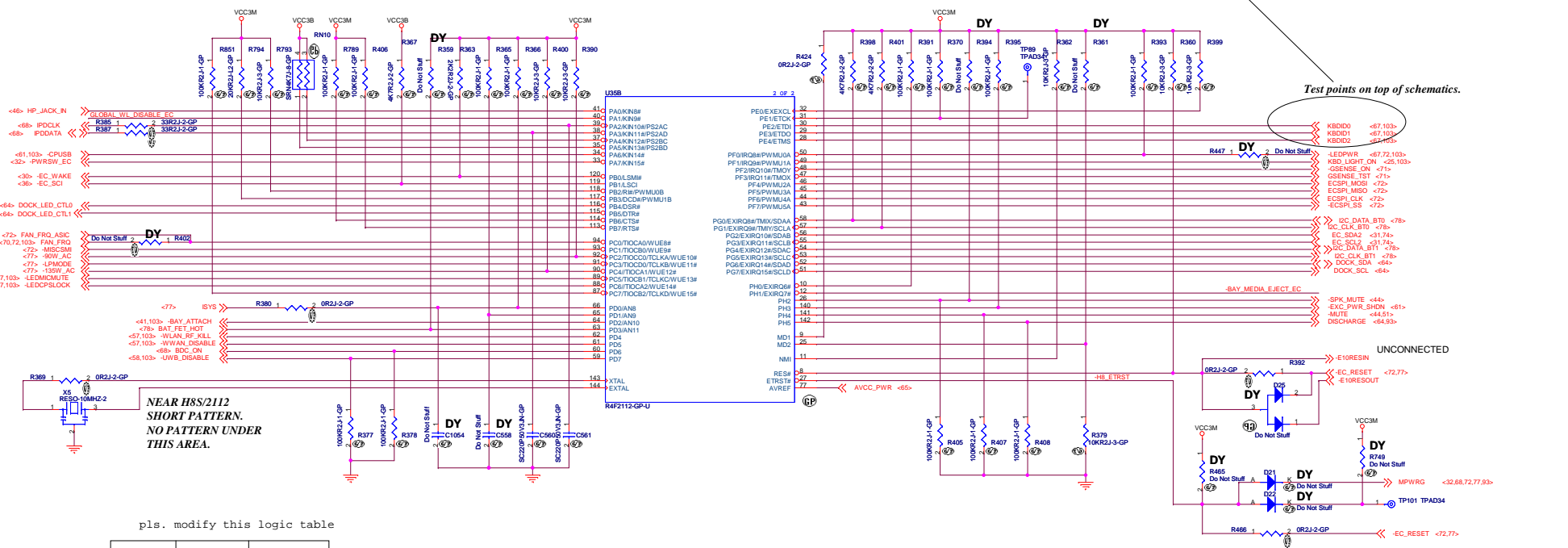
TEST PAD X2
BOTTOM SIDE
DO NOT MOVE AFTER FIX



DO NOT MOVE



Power rail for RN10 can be either VCC3B or VCC5B.



NEAR H8S/2112
SHORT PATTERN.
NO PATTERN UNDER
THIS AREA.

pls. modify this logic table

E10A Debug I/F	Enable	Disable
R368	NO ASM	ASM
R380	NO ASM	ASM
D20	ASM	NO ASM
D22	ASM	NO ASM
D21	ASM	NO ASM
R360	ASM	ASM
R361	ASM	NO ASM
R620	ASM	NO ASM
R372	ASM	NO ASM
R379	NO ASM	ASM

LOGIC

Test points on top of schematics.

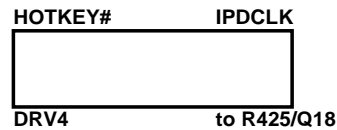
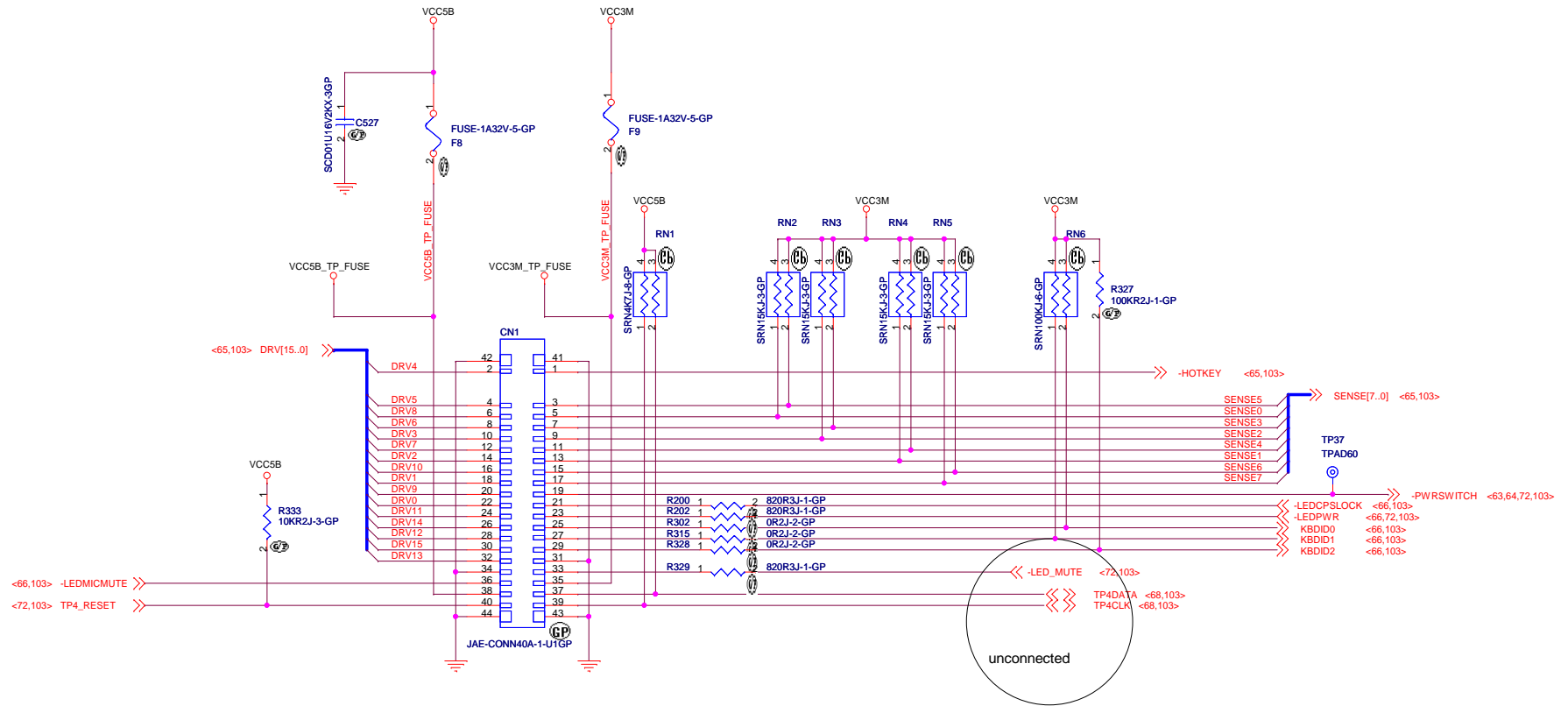
UNCONNECTED

BOM1

緯創實通 Wistron Corporation	
21F, 8B, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
File H8S2112(2/2)	
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Keyboard Connector



Keyboard Connector Top View

BOM1

緯創資通		Wistron Corporation 21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
KEYBOARD CONN			
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BOM1

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

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Size
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Document Number

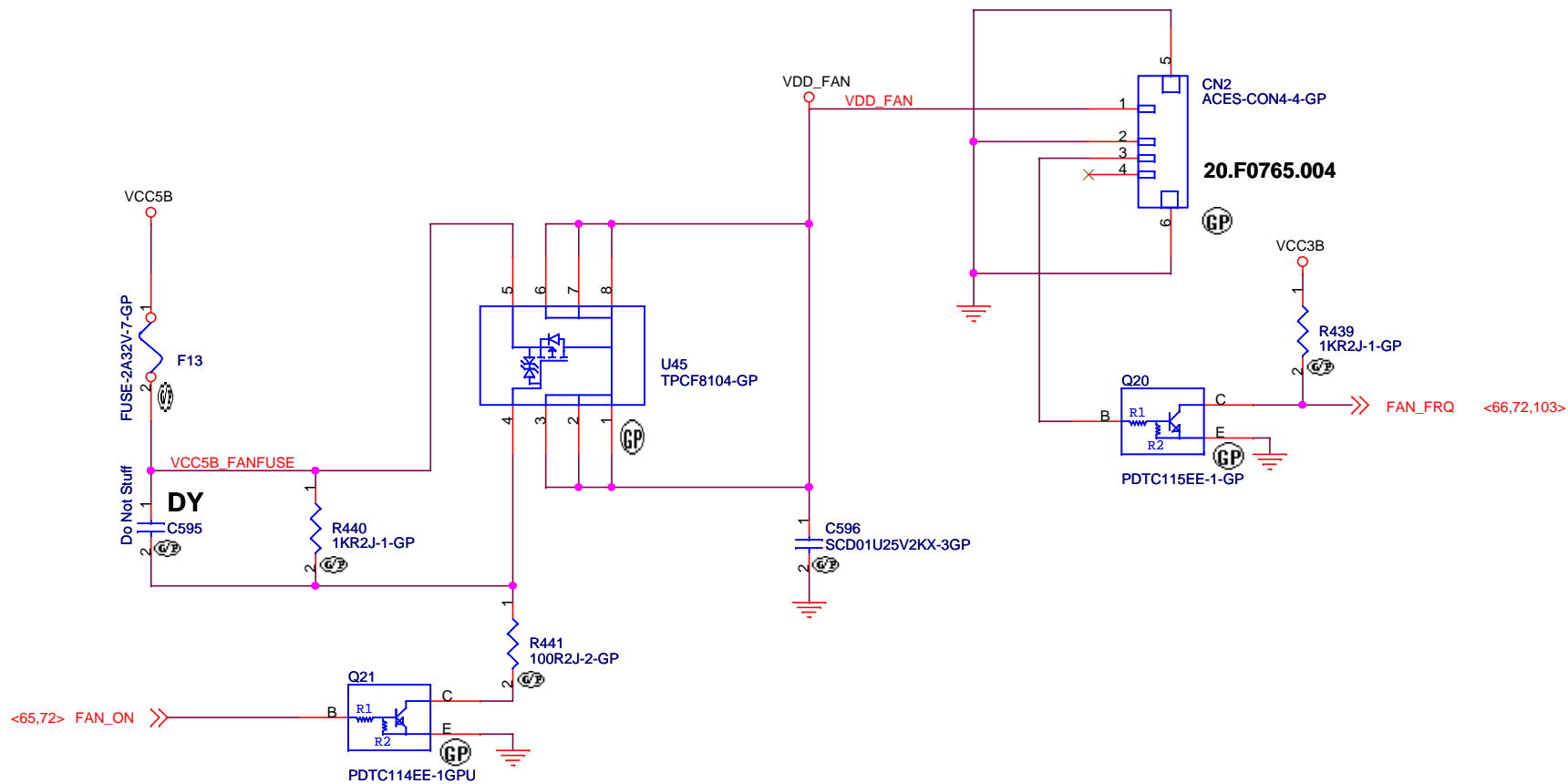
Kendo-1-UMA

Rev
SB


Date: Friday, March 13, 2009

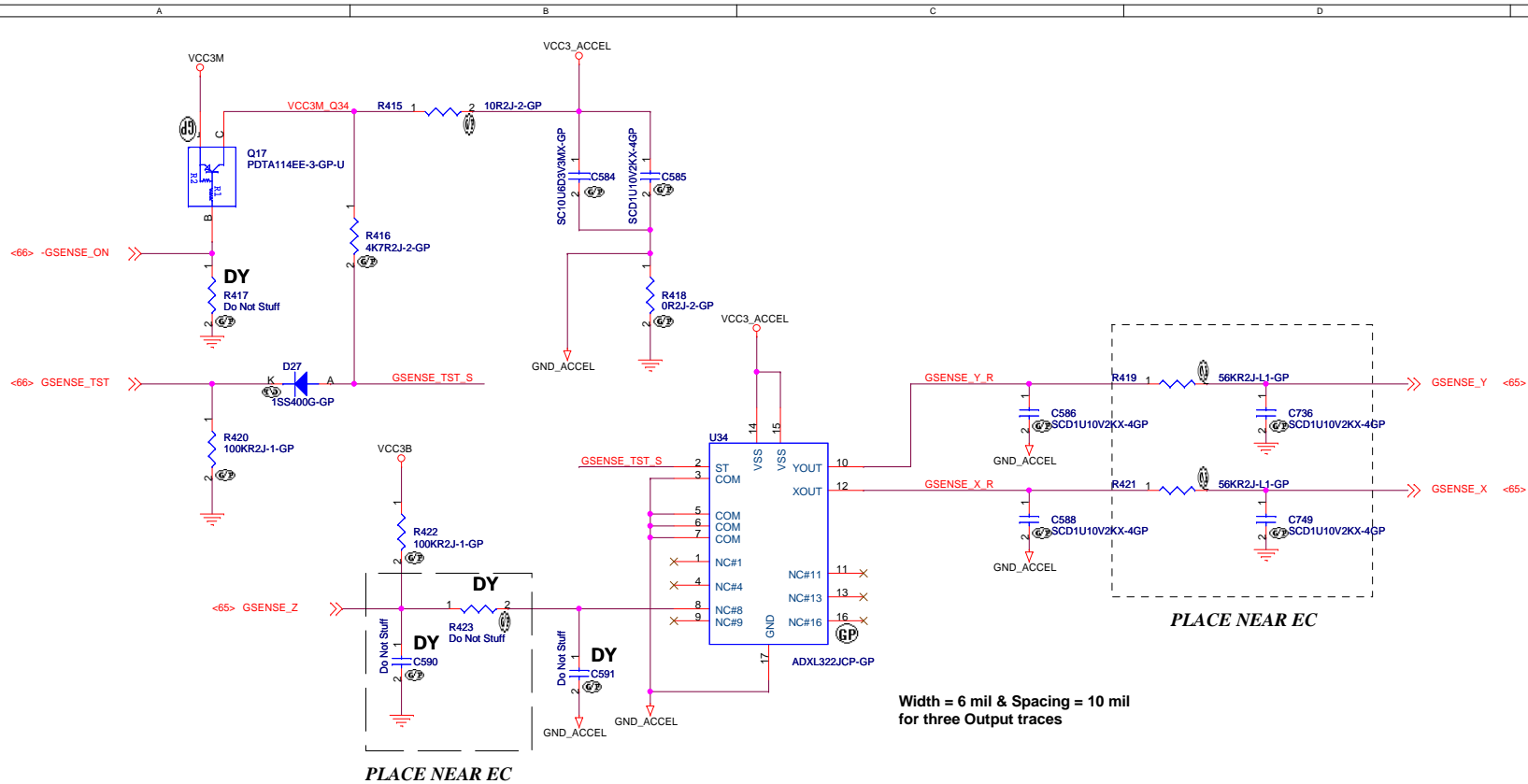
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BOM1

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
FAN CONTROL			
Size A4	Document Number Kendo-1-UMA		Rev SB
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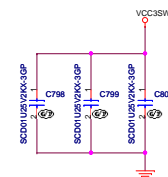
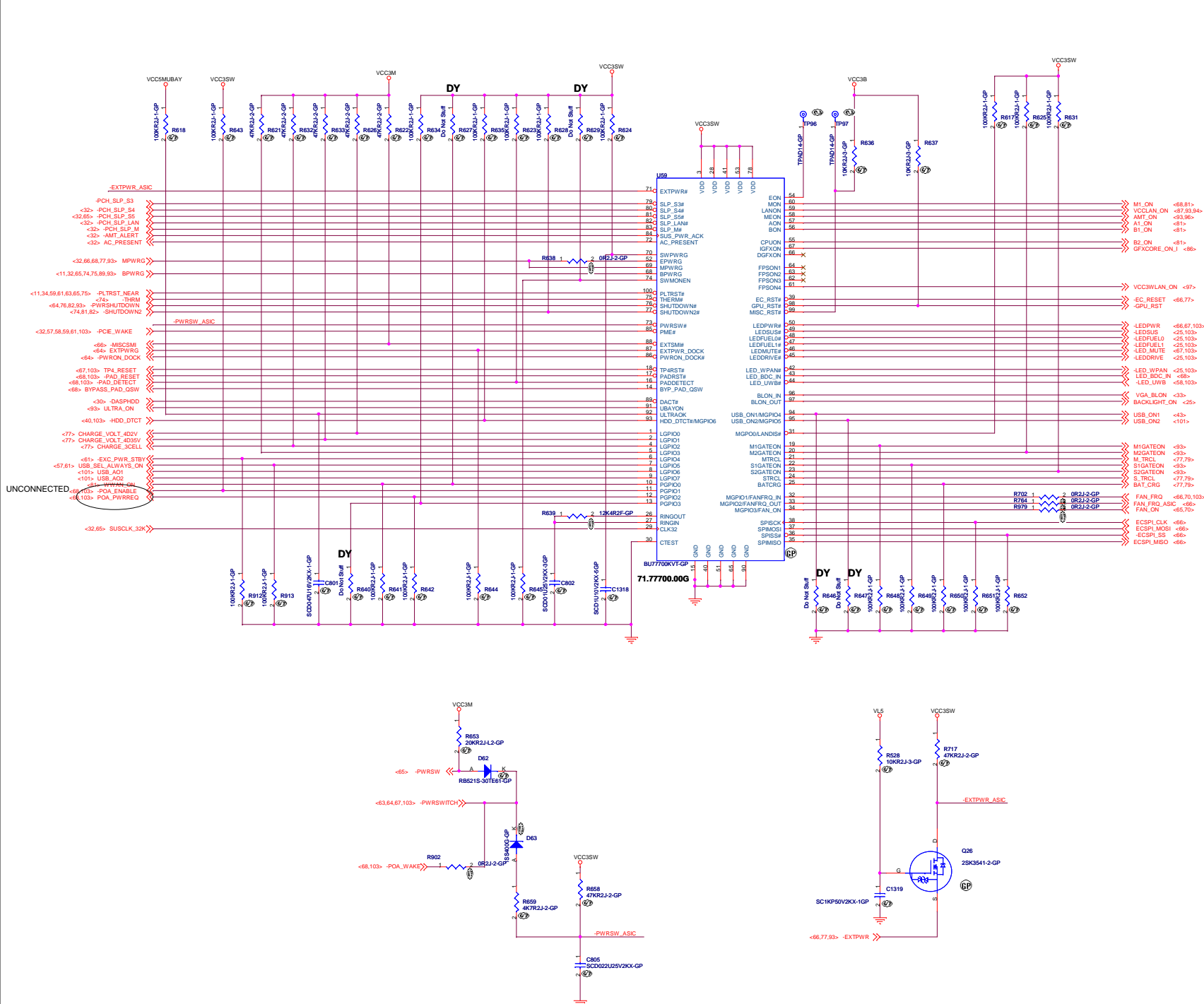
ADXL322/LIS244AL LIS34AL	NO ACC.
R417	NO-ASM
R420	ASM
U34	ASM
Q17	ASM
D27	ASM
R416	ASM
R415	10-OHM
C585	ASM
C584	ASM
C586	ASM
C588	ASM
C587	56K
C419	ASM
R421	56K
C589	ASM
C591	NO-ASM
R423	NO-ASM
C590	NO-ASM
R422	ASM

Layout Comment :
 (1) Place C586, C588, Q17, R415, R417, C584, C585, R420 close to U34.
 (2) Avoid routing under DCDC switching area.

BOM1

緯創資通		Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
G-SENSOR			
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BOM1

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Title

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Size
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Document Number

Kendo-1-UMA

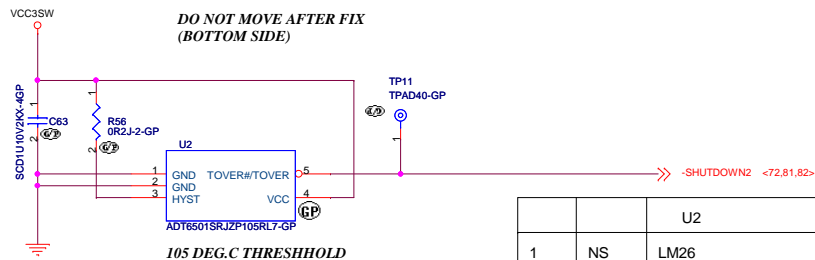
Rev
SB

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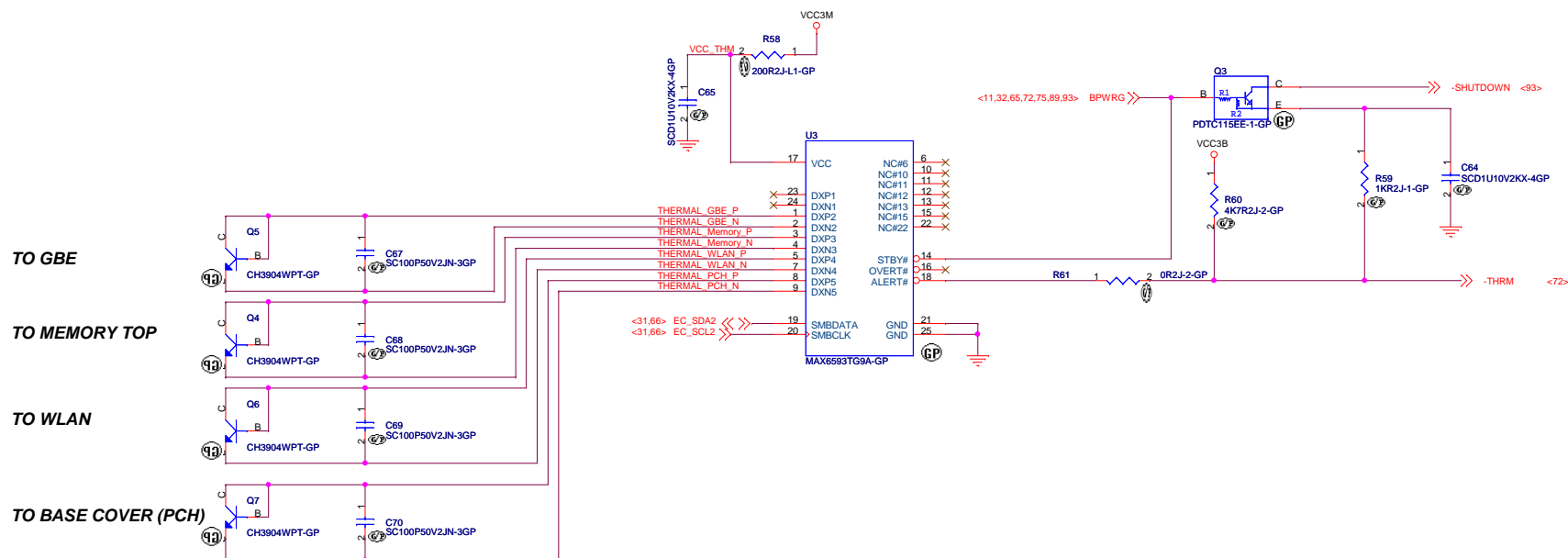
<https://Dr-Bios.com>

Thermal Sensor for CPU



		U2	R56
1	NS	LM26	NO_ASM
2	MAXIM	MAX6519	NO_ASM
3	ADI	ADT6501SRJZP105RL7	ASM
4	MAXIM	MAX6501UKP105+1	ASM

THESE CAPS MUST BE PLACED AS
CLOSE AS POSSIBLE TO MAX6593



MAX6593

Remote 1: Discrete GFX Address 01h.(Discrete only)

Remote 2: GigaBitEthernet Address 02h.

Remote 3: SODIMM Top Address 03h.

Remote 4: Wireless LAN Slot Address 04h.

Remote 5: Base Cover (Under PCH) Address 05h.

Local: SODIMM Bottom Address 07h.

PLACE UNDER PC-CARD SLOT
HS I2C Bus 2 ADDRESS : 4DH
TEST PAD FOR BOARD MFG TEST

Layout Comment :

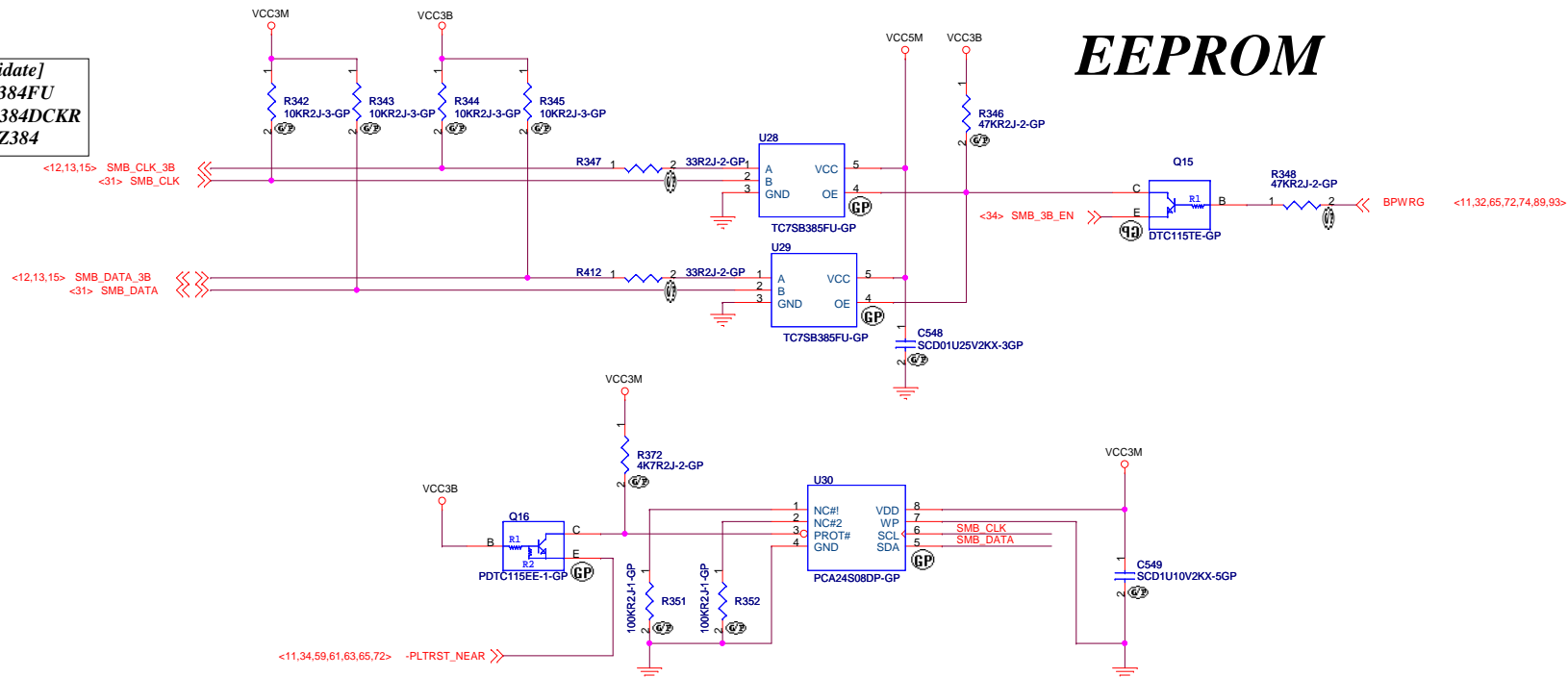
- (1) Thermal sensor trace lines should not be overlapped with other high frequency trace lines in other layers.
- (2) Also, it should not be overlapped with large amplitude trace lines either.

BOM1

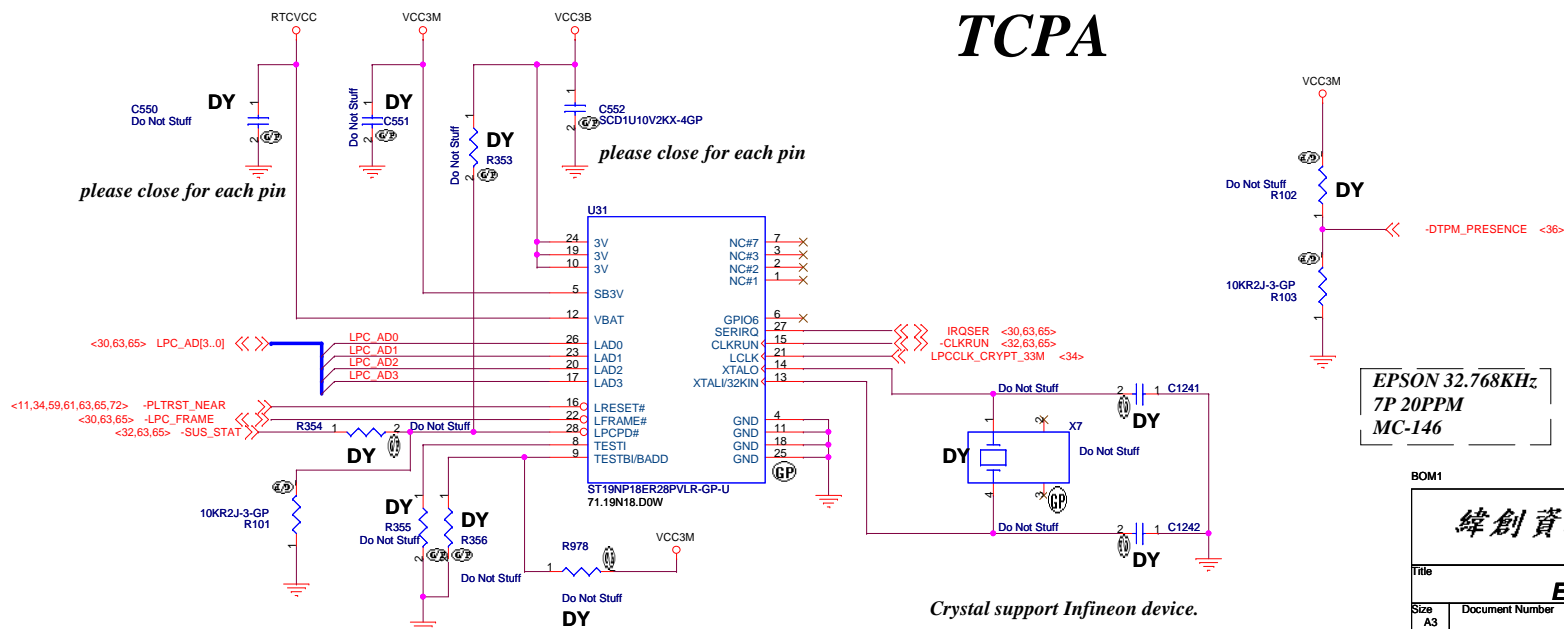
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
THERMAL SENSOR	
Title Size Custom	Document Number Kendo-1-UMA
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[Sourcing Candidate]
 Toshiba TC7SB384FU
 TI SN74CBT1G384DCKR
 Fairchild NC7SZ384

EEPROM



TCPA



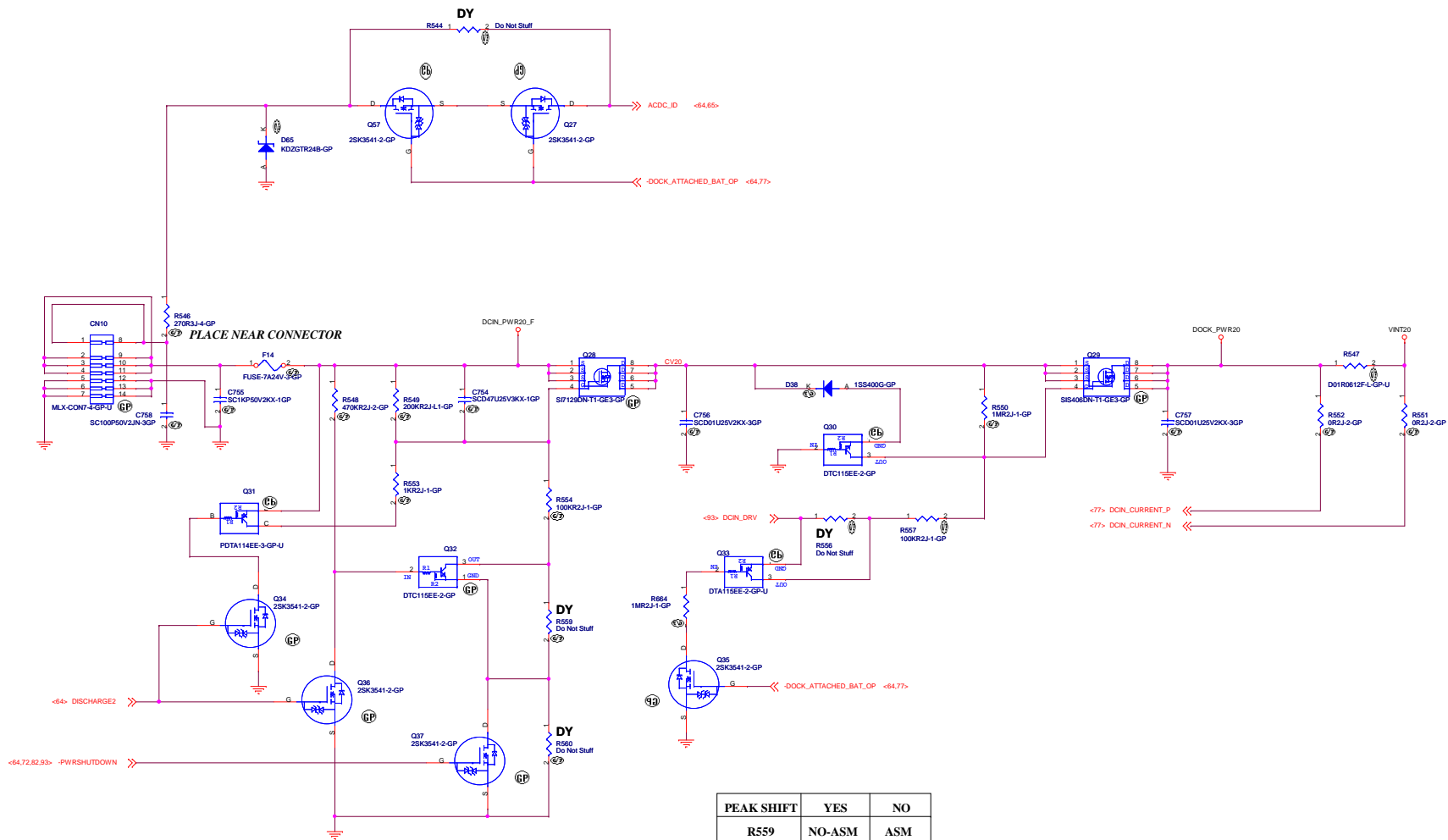
EPSON 32.768KHz
 7P 20PPM
 MC-146

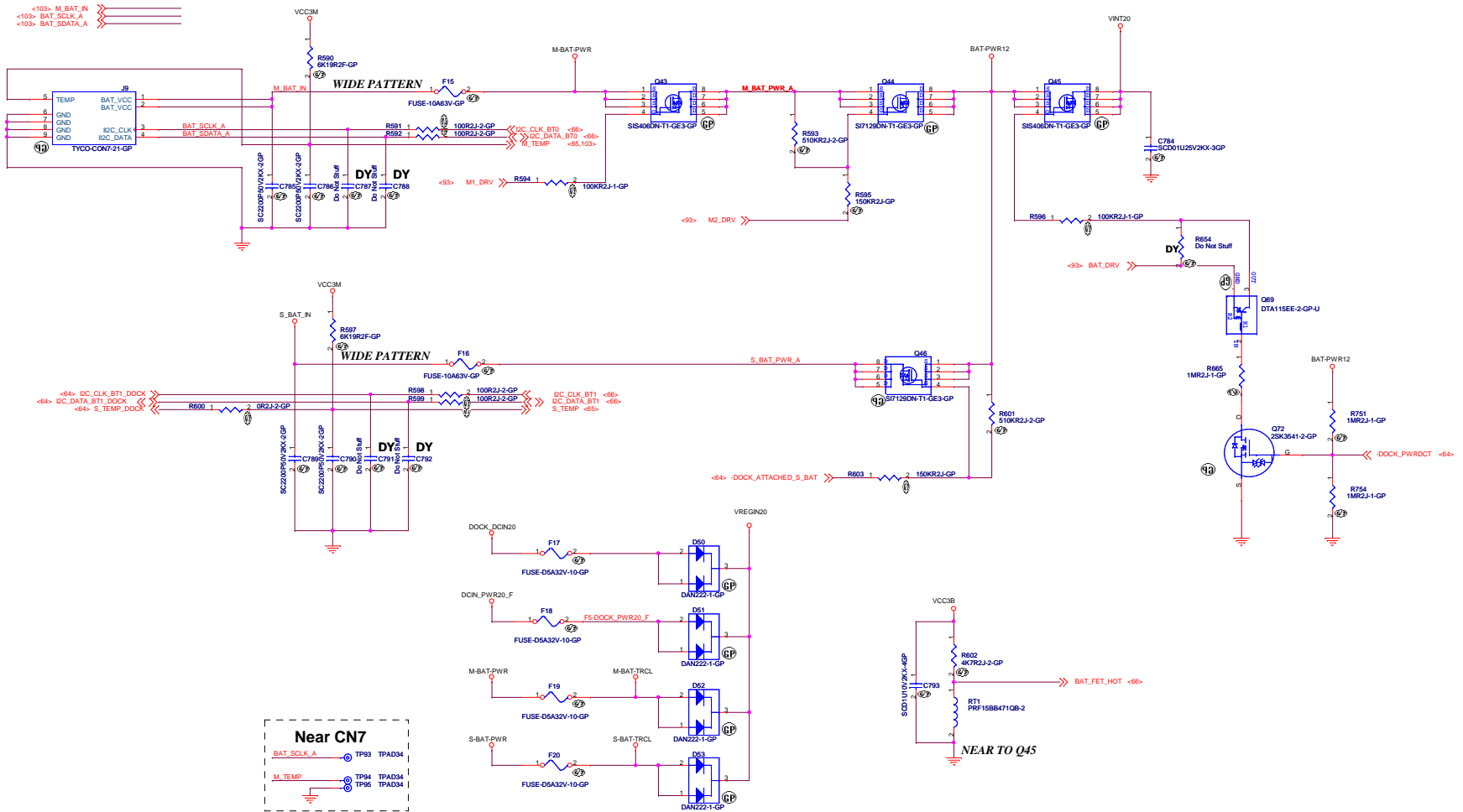
BOM1

緯創資通		Wistron Corporation	
		21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
EEPROM/TCPA			
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Crystal support Infineon device.

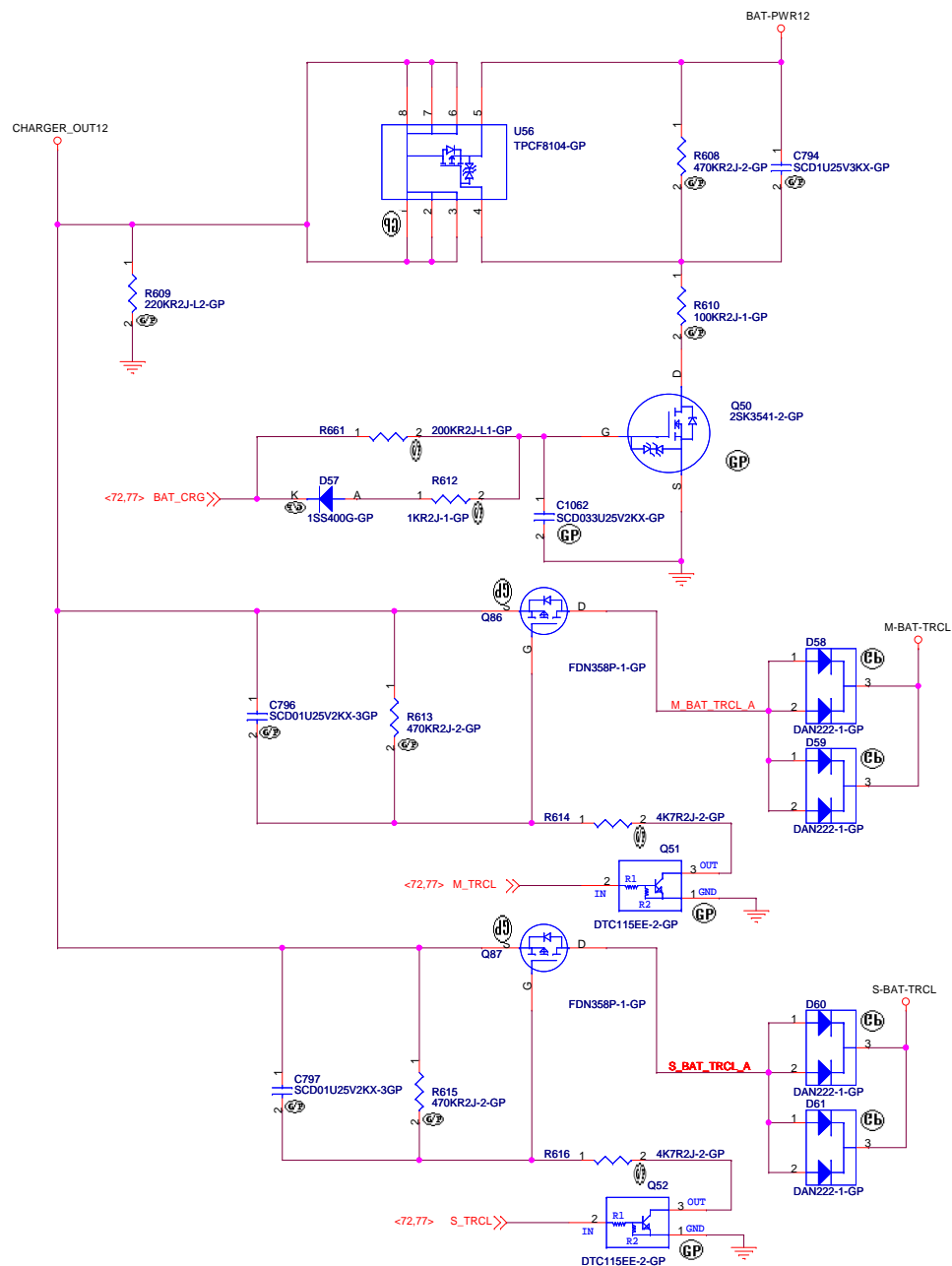




BCM1

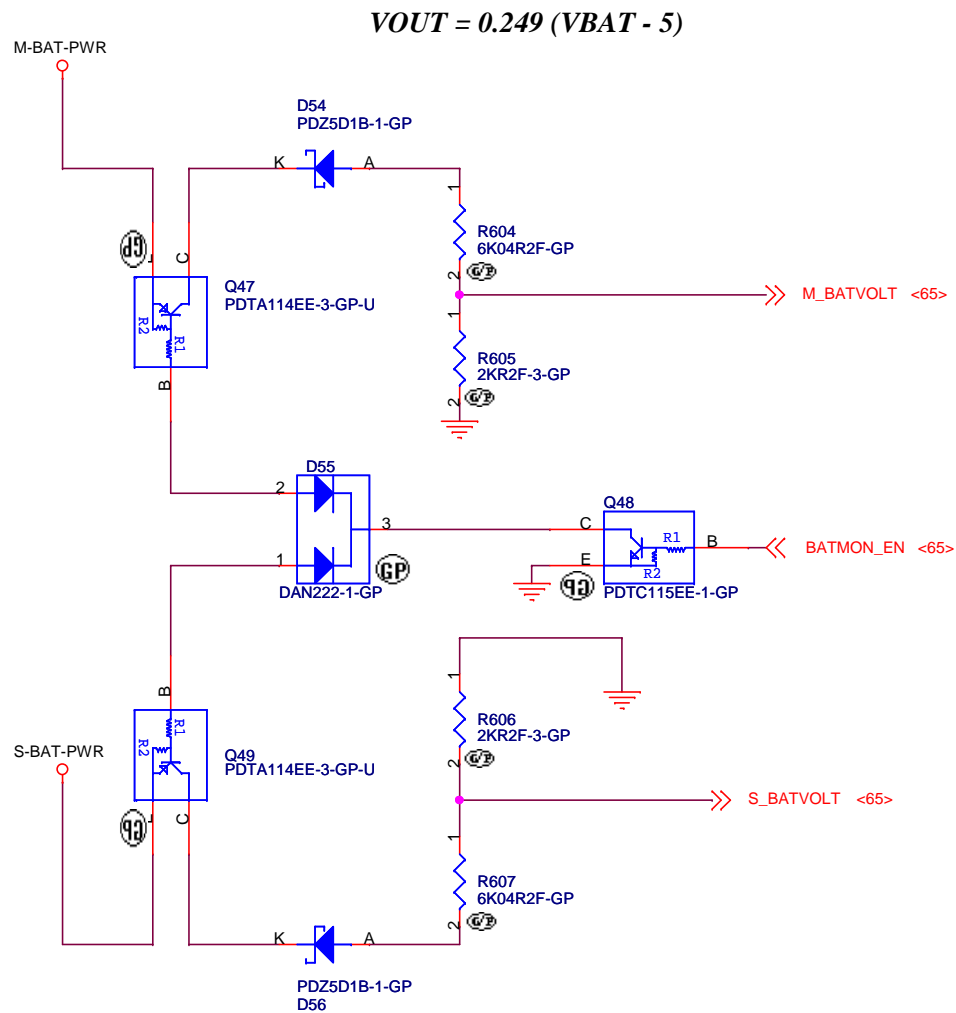
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai W6 Rd., Neichih,
Taipei Hsien 221, Taiwan, R.O.C.

BATTERY INPUT		
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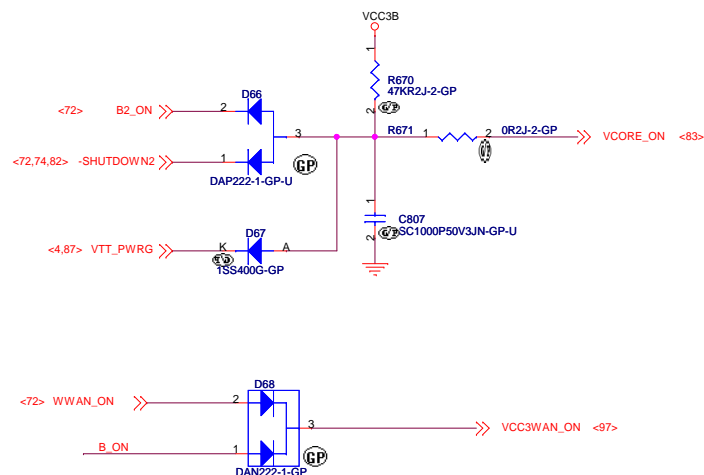
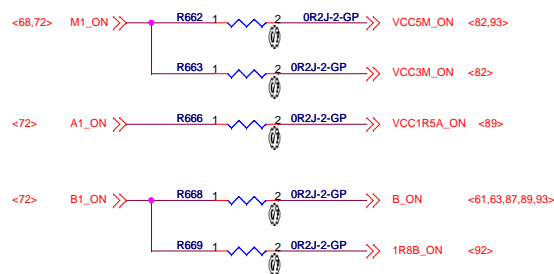
BOM1

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
CHARGER SELECT		
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BOM1

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
BATTERY MONITOR			
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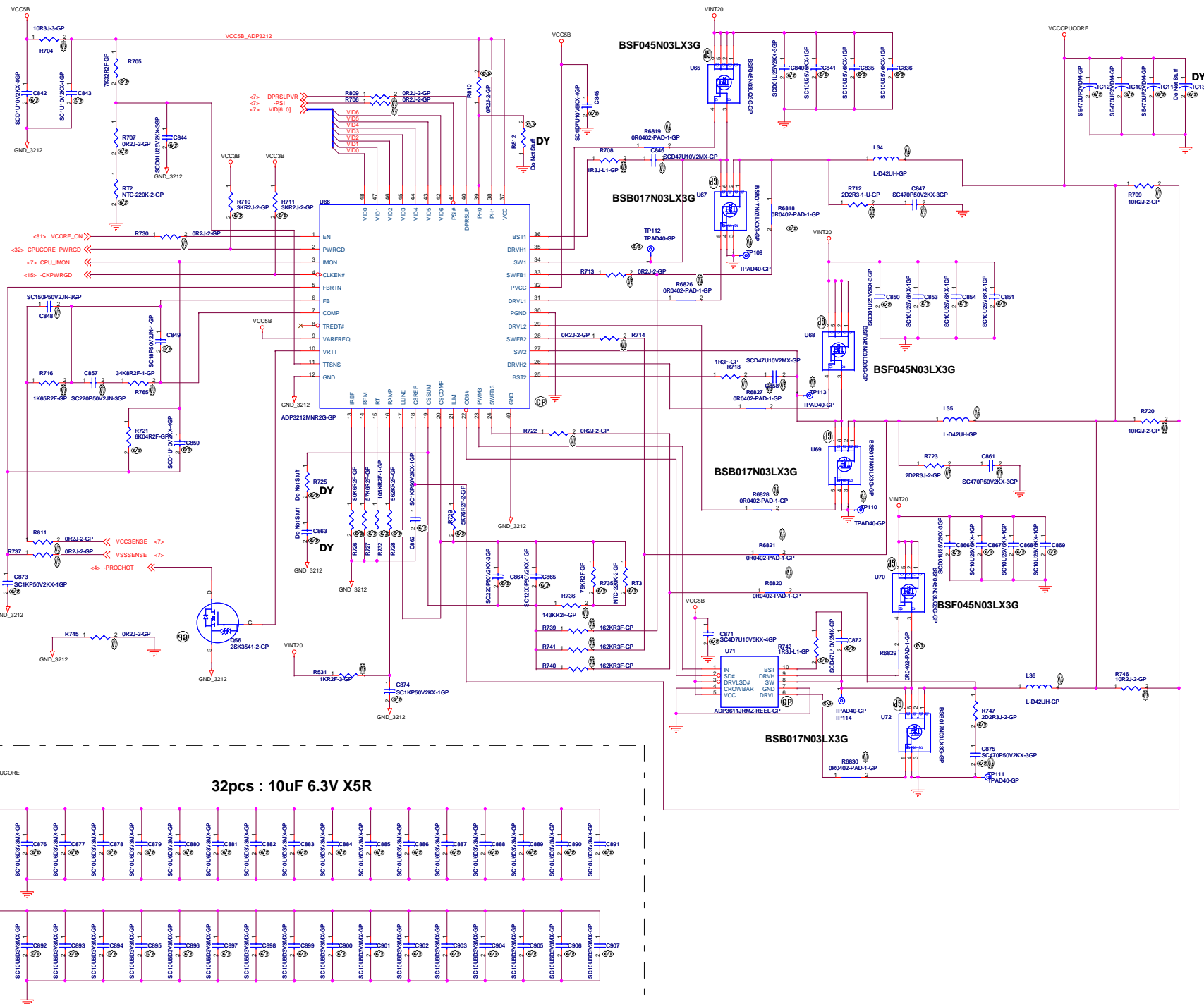


Table

ALWAYS ON	YES	NO
D68	ASM	NO_ASM

BOM1

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
POWER SEQUENCE	
Title Size A3 Date: Friday, March 13, 2009	Document Number Kendo-1-UMA Sheet 81 of 104
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BOM1

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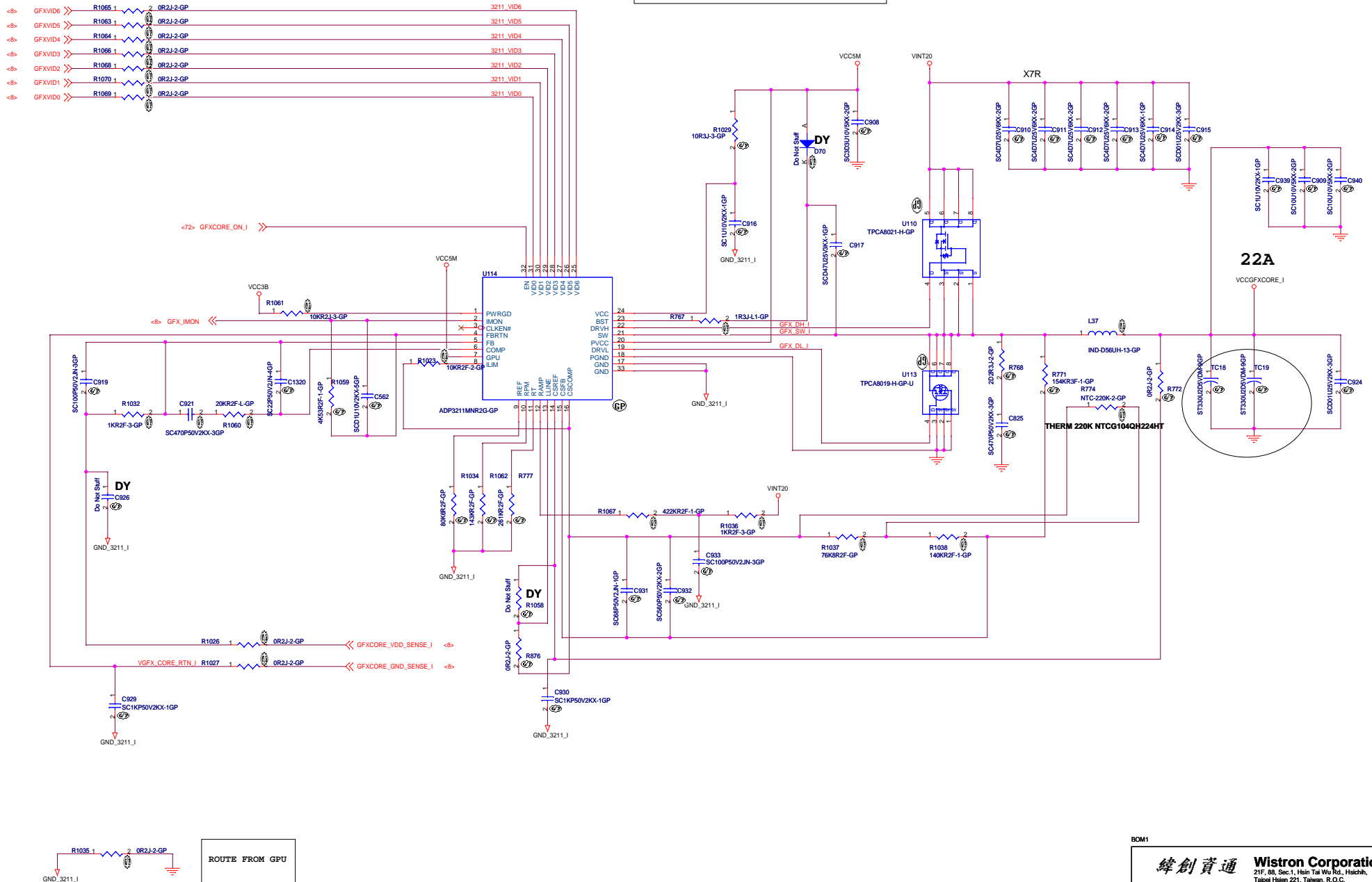
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INTEG VCCGFXCORE VOLTAGE
1.NORMAL; 1.05V
2.GRAPHICS RENDER STANDBY MODE; 0.75V
VID <4..0>
01000 = 1.05V

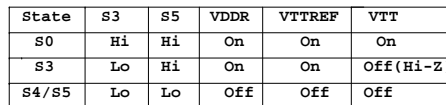


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BOM1

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
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State	S3	S5	VDDR	VTTREF	VTT
S0	Hi	Hi	On	On	On
S3	Lo	Hi	On	On	Off(Hi-Z)
S4/S5	Lo	Lo	Off	Off	Off

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BOM1

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title		BLANK	
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BOM1

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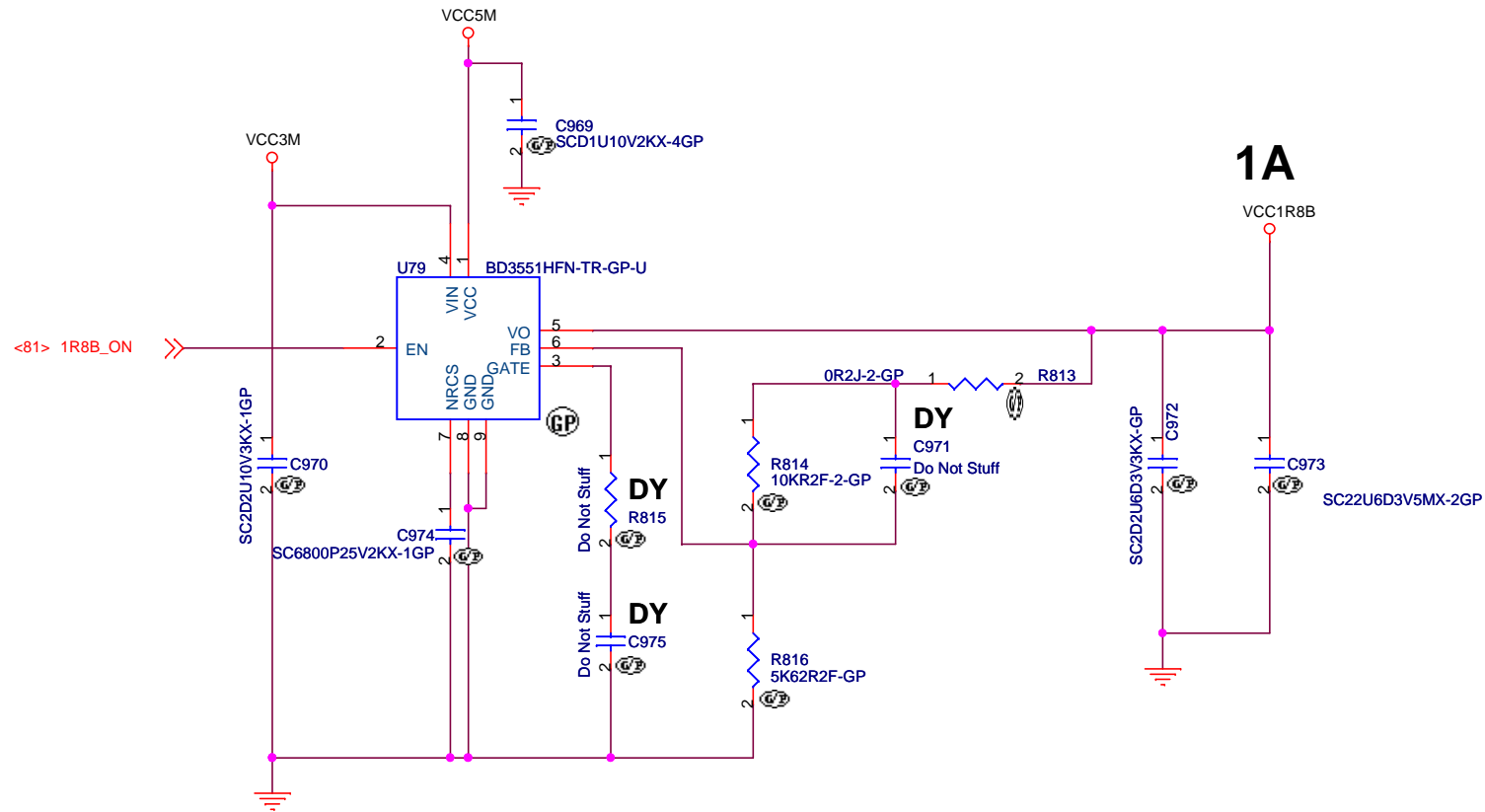
Kendo-1-UMA

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Date: Friday, March 13, 2009

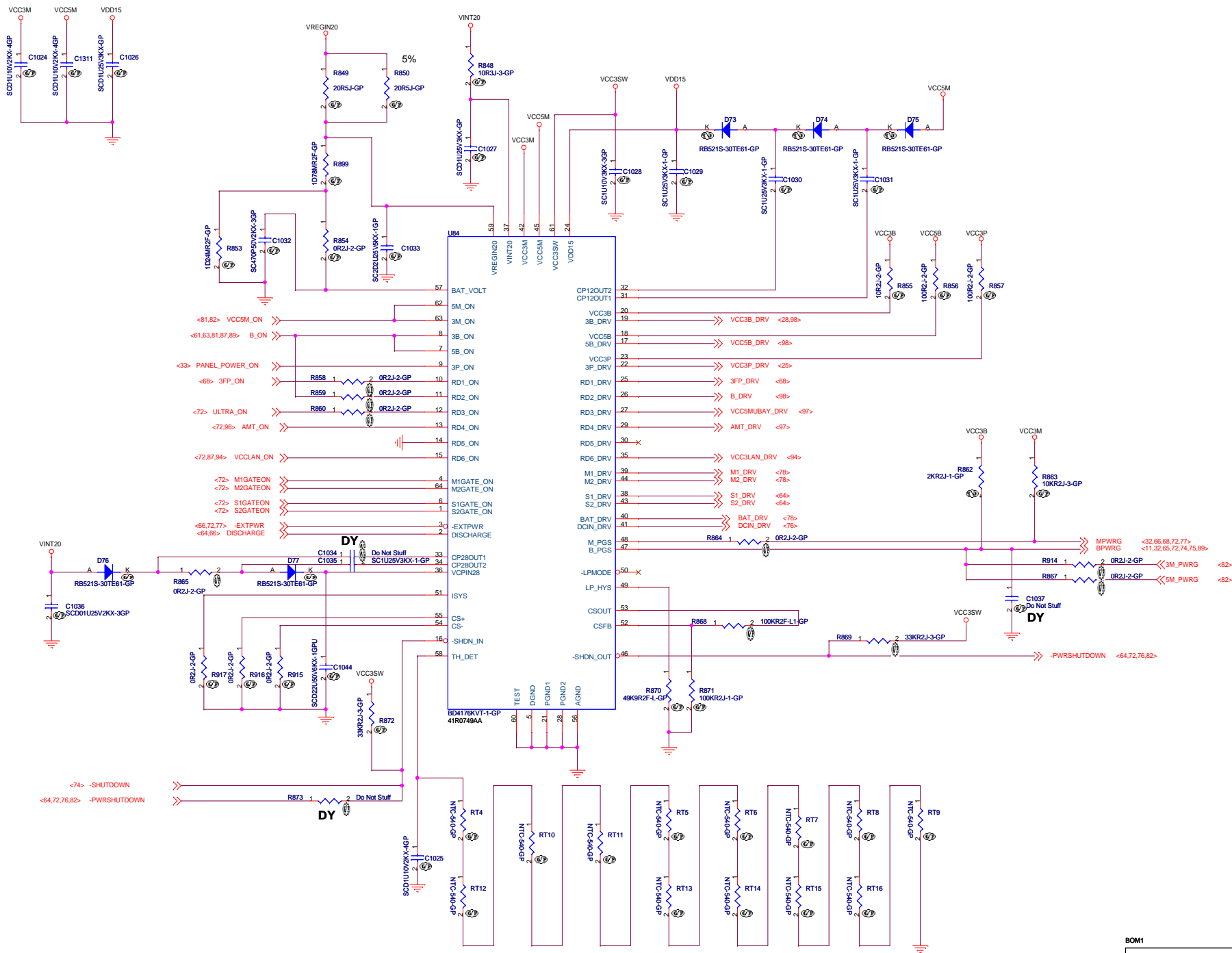
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<https://Dr-Bios.com>



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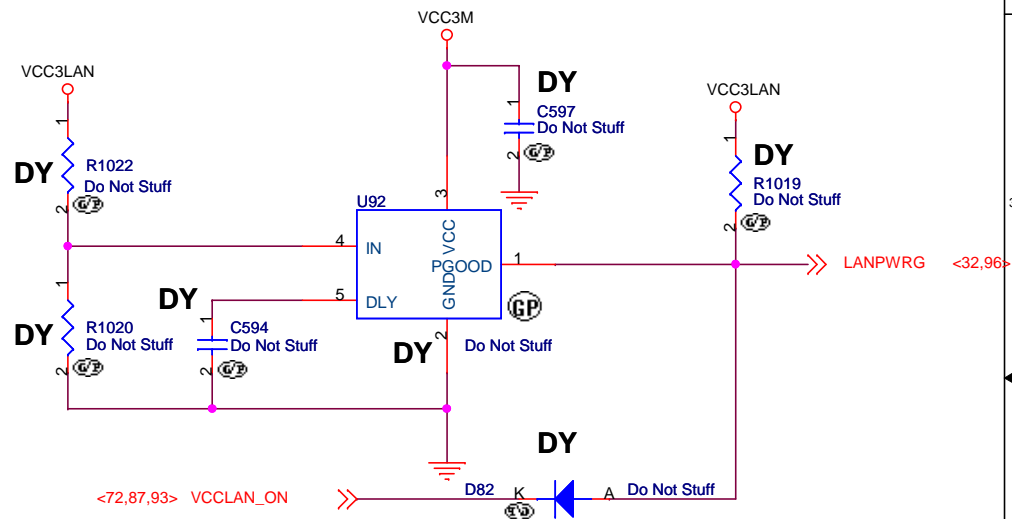
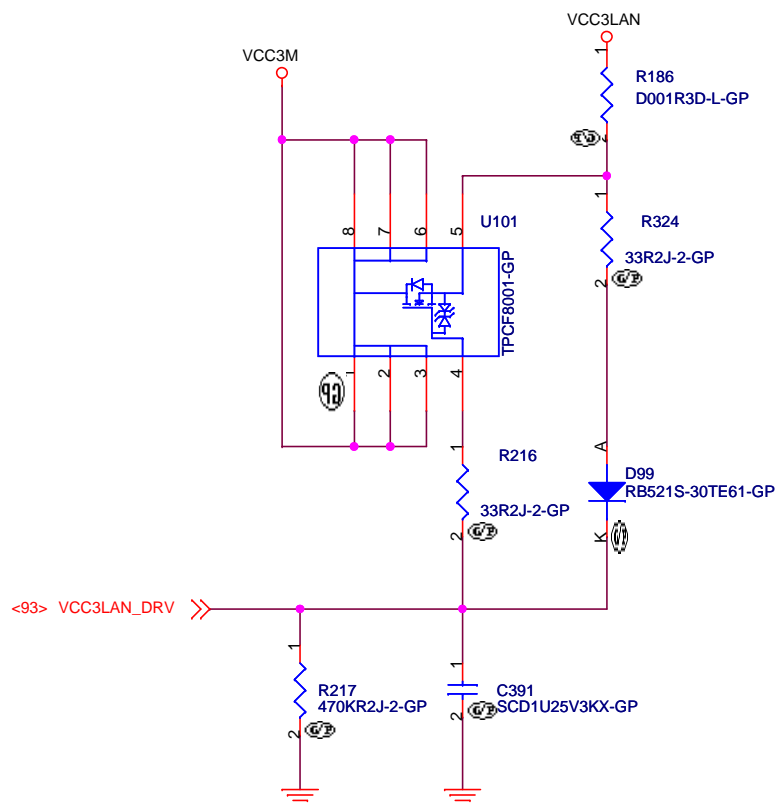
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Title			
DC-DC VCC1R8B			
Size A4	Document Number		Rev SB
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BOM1

緯創資通		Wistron Corporation	
21F, 8B, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		Title	
DC-DC RINKAN-2		Document Number	
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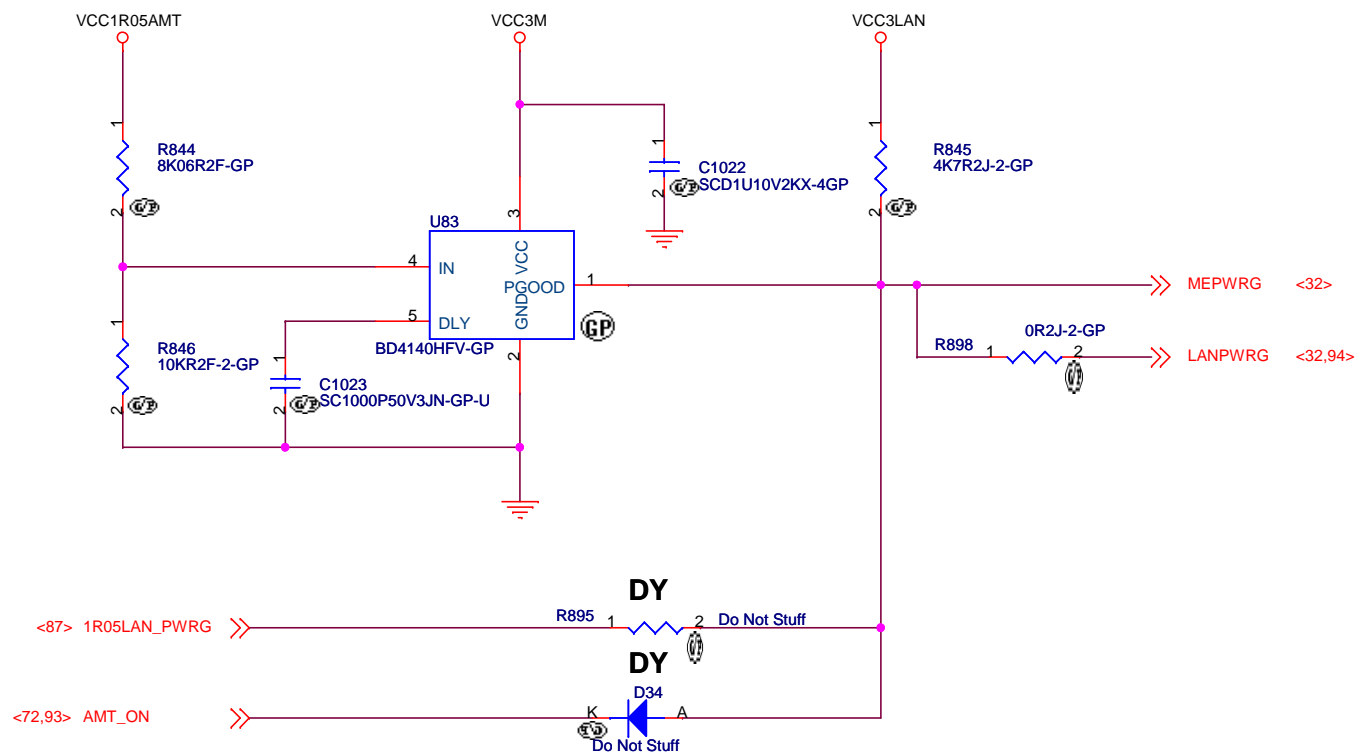
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Size A4	Document Number		Rev SB
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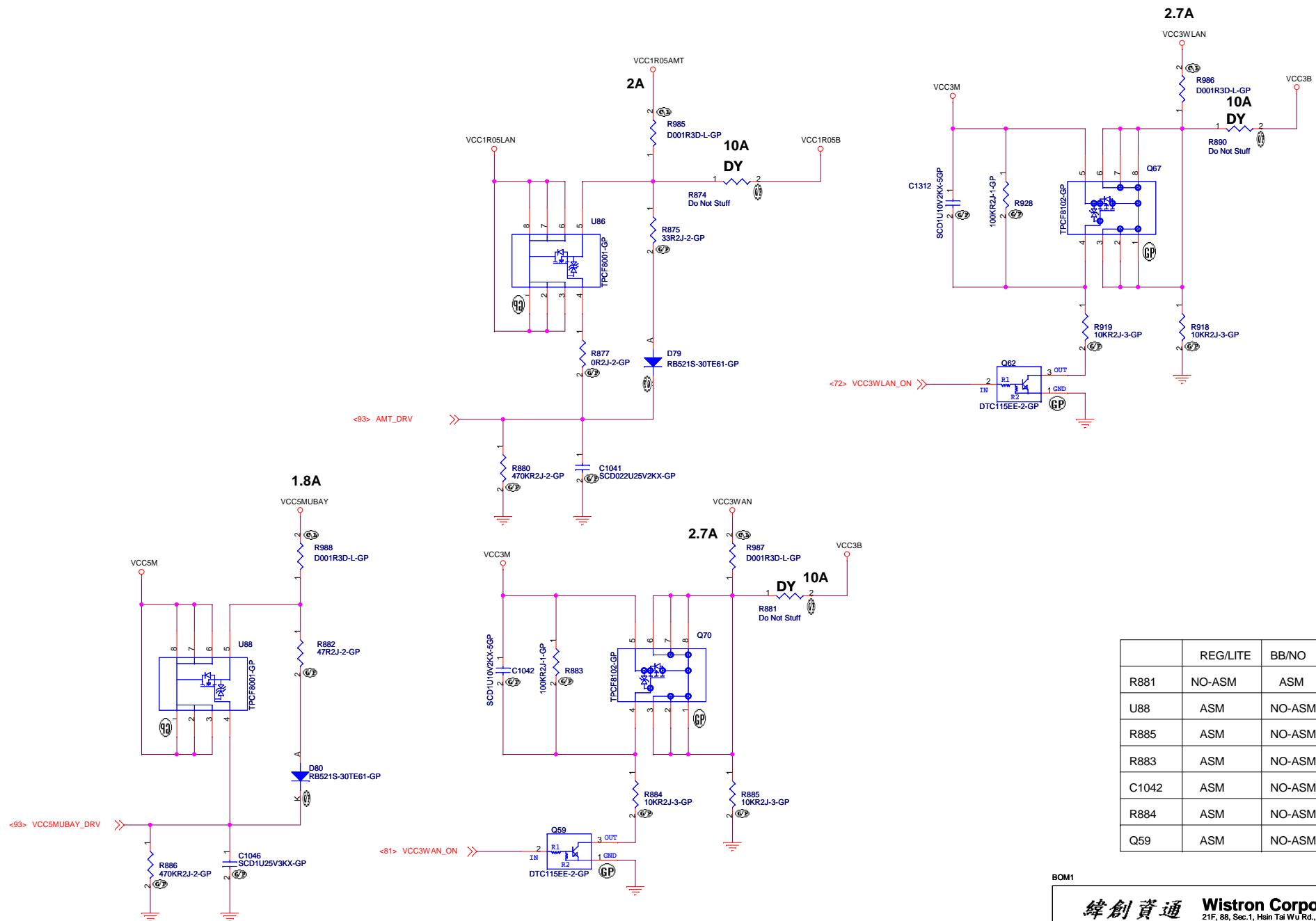
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Size A	Document Number		Rev
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BOM1

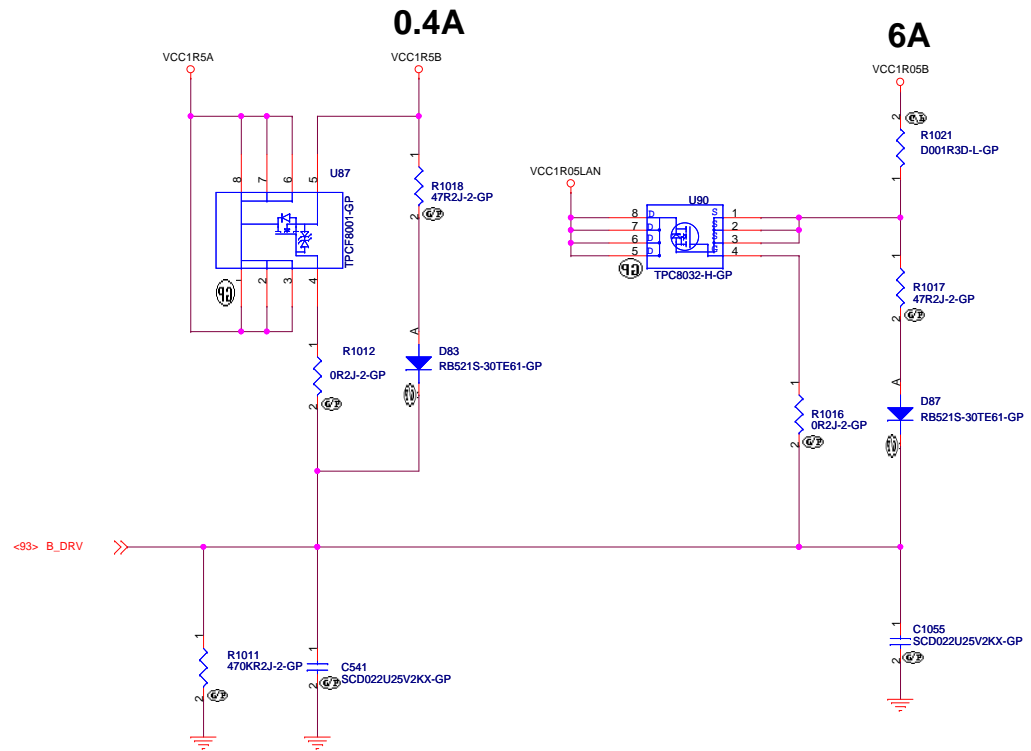
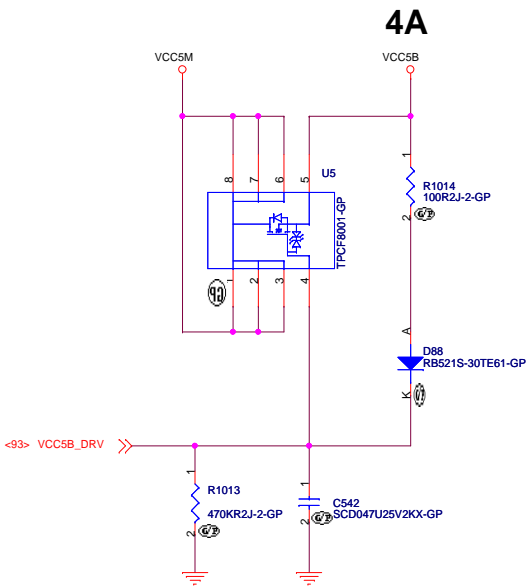
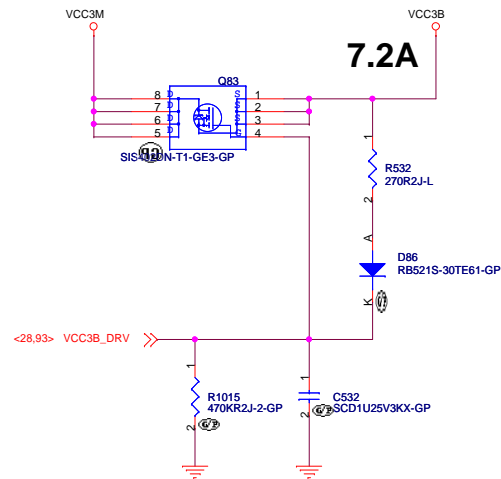
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Title	
MEPWRG	
Size A4	Document Number
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	REG/LITE	BB/NO
R881	NO-ASM	ASM
U88	ASM	NO-ASM
R885	ASM	NO-ASM
R883	ASM	NO-ASM
C1042	ASM	NO-ASM
R884	ASM	NO-ASM
Q59	ASM	NO-ASM

BOM1

<div> <div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div> </div>		
Title		
LOAD SW B		
Size	Document Number	Rev
A3	Kendo-1-UMA	SB
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緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
LOAD SW1			
Size A3	Document Number		Rev SB
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緯創資通

Wistron Corporation

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Title

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A

Document Number

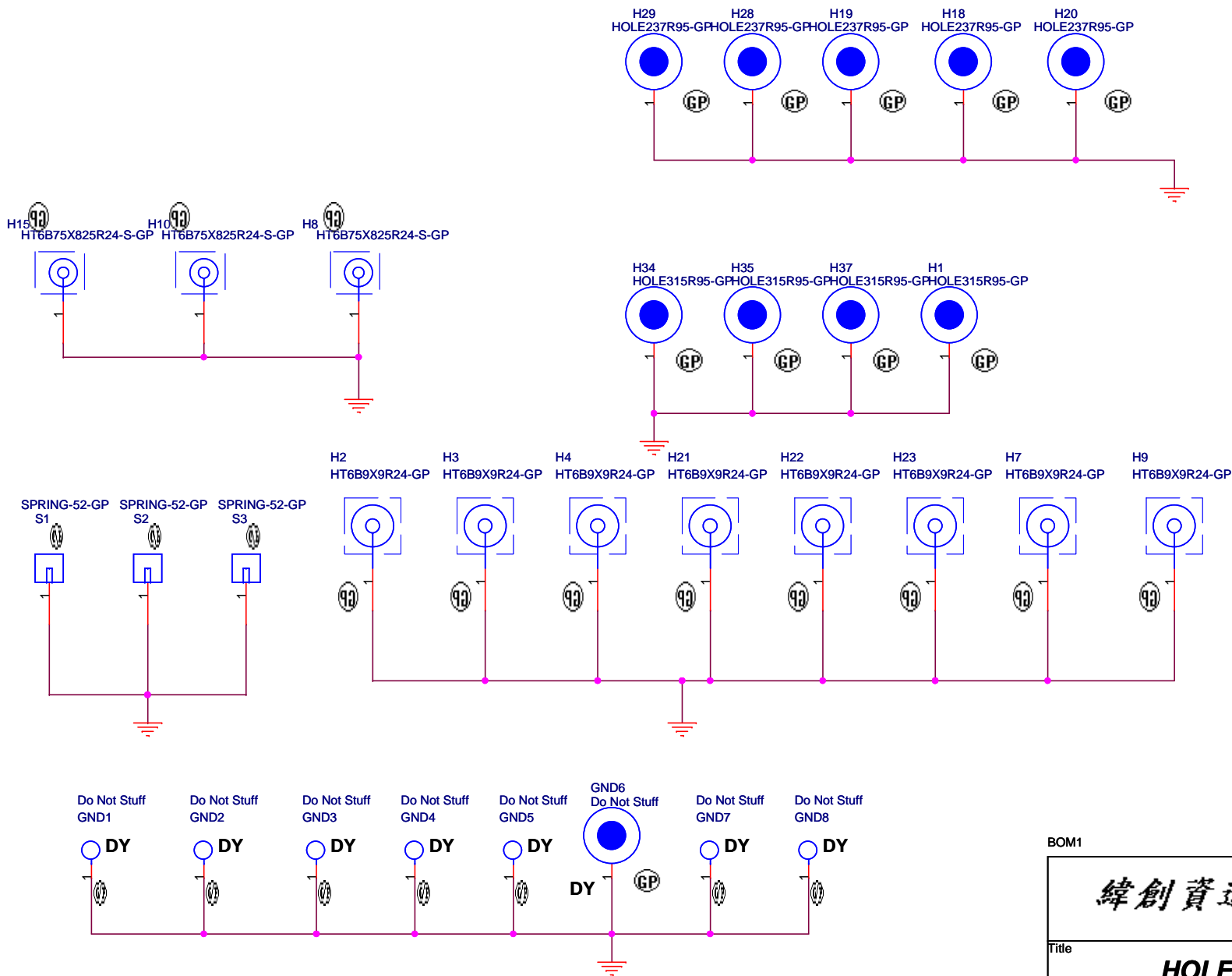
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Rev
SB


Date: Friday, March 13, 2009

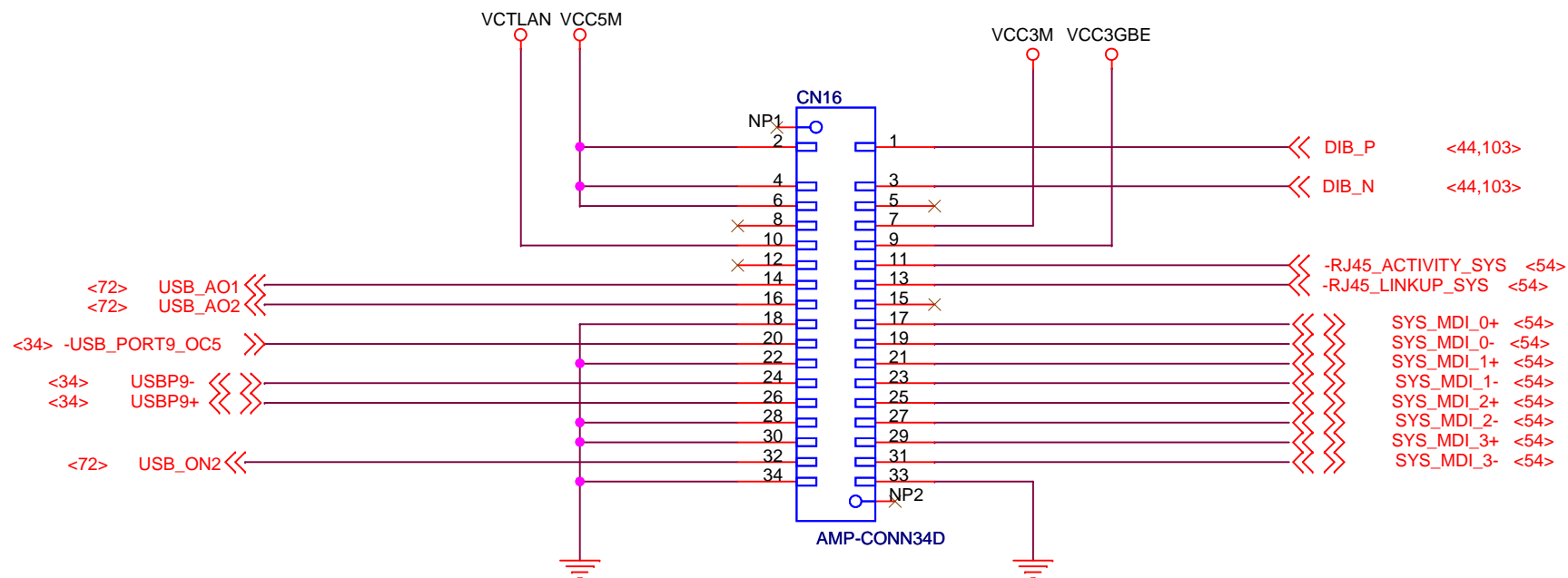
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BOM1

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Title			
HOLES/GND/PADS			
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20.F0748.034

Pull-down resistor is necessary on USB_AO1/2 for PI3USB14LE on the USB BRD.

BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

USB BOARD CONNECTOR

Size
A

Document Number

Kendo-1-UMA

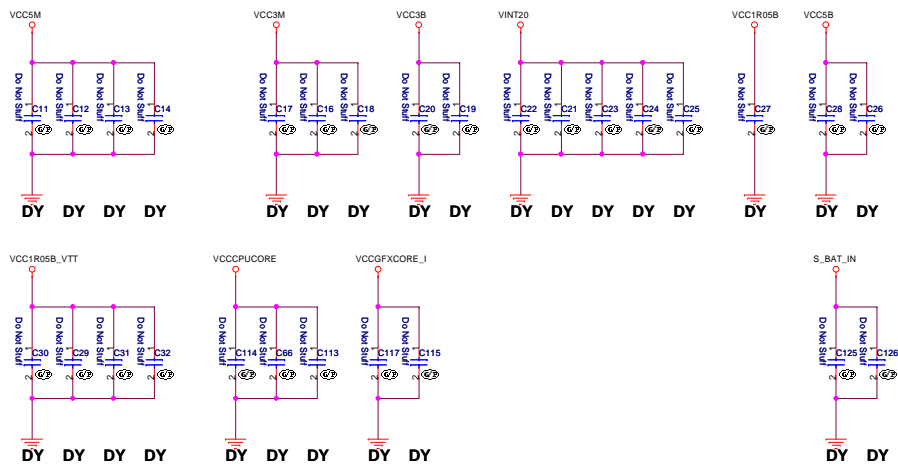
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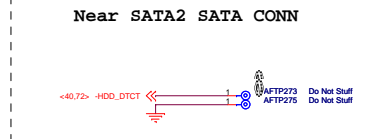
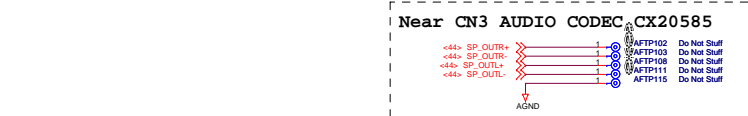
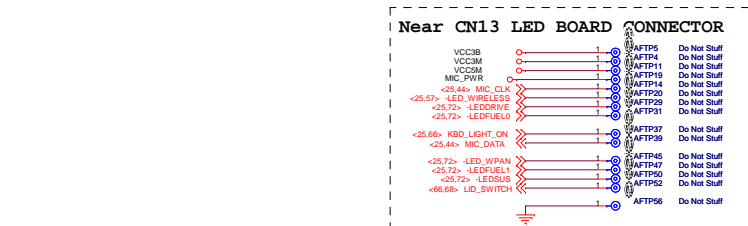
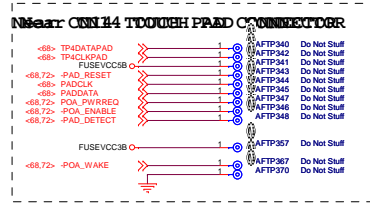
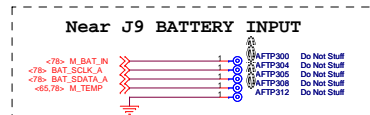
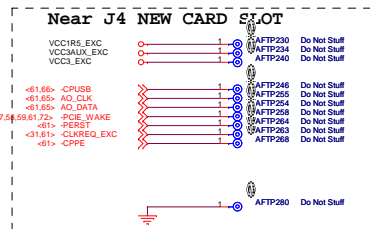
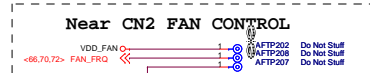
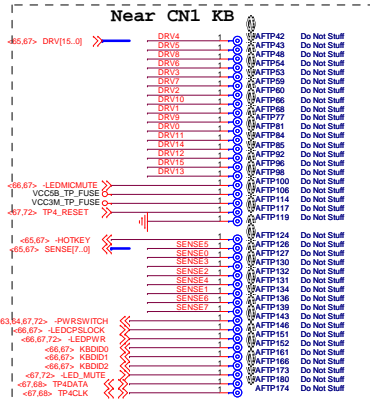
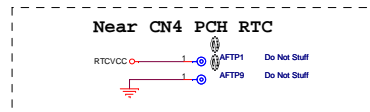
Long power trace EMI decoupling caps



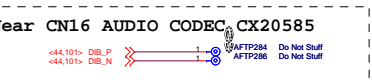
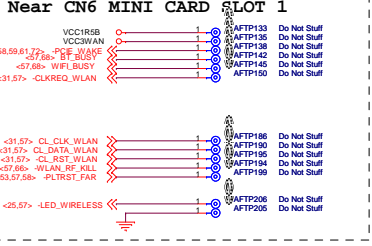
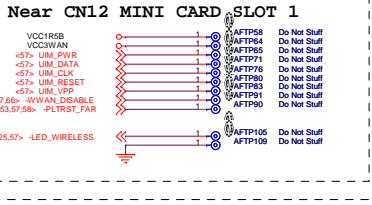
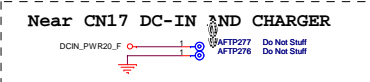
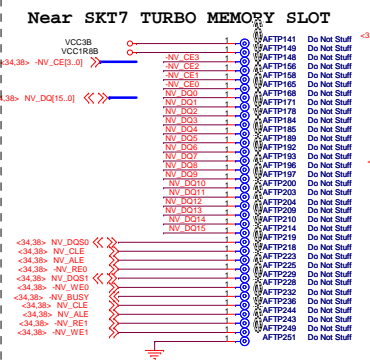
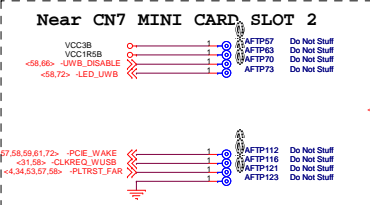
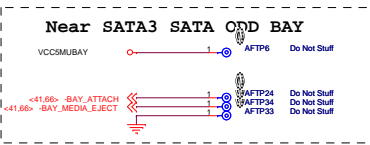
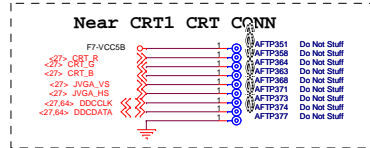
BOM1

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
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Title			EMI DECOUPLING
Size	Document Number		
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Near SATA3 SATA ODD BAY



From Aurburndale

MA_A_DQ[63:0]
M_ A_DM[7:0]
-M_ A_DQS[7:0]
M_ A_DQS[7:0]
M_A_A[15:0]
-DRAMRST
-M_A_RAS
-M_A_CAS
-M_A_WE
M_A_BS0
M_A_BS1
M_A_BS2
DDRCLK0_533M
-DDRCLK0_533M
DDRCLK1_533M
-DDRCLK1_533M

M_CKE0
M_CKE1

-M_CS0
-M_CS1

M_ODT0
M_ODT1

-MEM_TS0
SMB_DATA_3B
SMB_CLK_3B

SODIMM CH-A Primary

From CAurburndale

MA_B_DQ[63:0]
M_ B_DM[7:0]
-M_ B_DQS[7:0]
M_ B_DQS[7:0]
M_B_A[15:0]
-DRAMRST
-M_B_RAS
-M_B_CAS
-M_B_WE
M_B_BS0
M_B_BS1
M_B_BS2
DDRCLK2_533M
-DDRCLK2_533M
DDRCLK3_533M
-DDRCLK3_533M

M_CKE2
M_CKE3

-M_CS2
-M_CS3

M_ODT2
M_ODT3

-MEM_TS1
SMB_DATA_3B
SMB_CLK_3B

SODIMM CH-B Primary

SODIMM IIC Address :
CH-A Primary : 50h
CH-B Primary : 51h

Pin1 on SODIM connector (VREF_DQ) only connects to DDR Voltage divider.
Clarksfield H17/J17 is left.

SCM1